

Workshop // on // Fast Realtime Systems // and // Realtime Machine Learning



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Versatile FPGA-based DAQ system using synchronous Aurora links and AXI infrastructure

Tuesday 9 April 2024 10:40 (20 minutes)

Presenter: KORCYL, Grzegorz (Jagiellonian University Crakow)

Session Classification: Fast DAQ and Trigger #4: FPGA