Workshop // on // Fast Realtime Systems // and // Realtime Machine Learning

Tuesday, 9 April 2024

Fast DAQ and Trigger #4: FPGA - Room I 020 (09:00 - 11:30)

time	[id] title	presenter
09:00	[26] Belle II DAQ Upgrade with PCIe40 (implementation, practical experience and performance)	LEVIT, Dmytro
09:40	[7] TDC on FPGA and application n the DAQ system of the HADES experiment	TRAXLER, Michael
10:40	[24] Versatile FPGA-based DAQ system using synchronous Aurora links and AXI infrastructure	KORCYL, Grzegorz