

Workshop // on // Fast Realtime Systems // and // Realtime Machine Learning

Tuesday, April 9, 2024

Fast DAQ and Trigger #4: FPGA - Room I 020 (9:00 AM - 11:30 AM)

time	[id] title	presenter
9:00 AM	[26] Belle II DAQ Upgrade with PCIe40 (implementation, practical experience and performance)	LEVIT, Dmytro
9:40 AM	[7] TDC on FPGA and application n the DAQ system of the HADES experiment	TRAXLER, Michael
10:40 AM	[24] Versatile FPGA-based DAQ system using synchronous Aurora links and AXI infrastructure	KORCYL, Grzegorz