Session Program

Apr 8 – 11, 2024

Workshop // on // Fast Realtime Systems // and // Realtime Machine Learning

Fast DAQ and Trigger #5

Heinrich-Buff-Ring 58, Room I 020
Justus-Liebig Universität Giessen, Campus Naturwissenschaften, Heinrich-Buff-Ring 58, 35392 Giessen. The building ("old" chemistry building) is the tallest building on the campus and thus easy to recognize. Opposite you find the cafeteria "CaRe". Lecture room is on the ground level. Please use left entrace and turn left immediately. There will be signs.

Tue, April 9

1:15 PM

Fast DAQ and Trigger #5

Session |

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1:15 – 1:45 PM **FPGA-based Belle II PXD readout (Jennifer2 Project Report)**

Speaker
Jens Soeren Lange

1:55 – 2:15 PM **FPGA Firmware for new Kintex UltraScale board (AXIStream)**

Speaker
Matthaeus Krein

2:20 – 2:40 PM **Cherenkov angle reconstruction on Xilinx Virtex**

Speaker
Schmidt Mustafa

2:45 PM