

Workshop // on // Fast Realtime Systems // and // Realtime Machine Learning

Tuesday, 9 April 2024

Fast DAQ and Trigger #5 - Room I 020 (13:15 - 14:45)

time	[id] title	presenter
13:15	[9] FPGA-based Belle II PXD readout (Jennifer2 Project Report)	LANGE, Jens Soeren
13:55	[10] FPGA Firmware for new Kintex UltraScale board (AXIStream)	KREIN, Matthaeus
14:20	[22] Cherenkov angle reconstruction on Xilinx Virtex	MUSTAFA, Schmidt