

Upgrade of the Neural Network Track Trigger for Belle II

Simon Hiesl Christian Kiesling Kai Unger Sebastian Skambraks Timo Forsthofer

Master student LMU

01.10.2024

Members of the Belle II Trigger Group





The Central Drift Chamber (CDC) of Belle II



The Belle II Detector



The CDC

• TS = Wire pattern compatible with a crossing track \rightarrow 2336 TS in 9 Super Layer (SL)



Simon Hiesl (Master student LMU)

Upgrade of the Neural Network Track Trigger

01.10.2024

The Old L1 Trigger Pipeline

- \bullet Present implementation \rightarrow 2DF inder and Neuro Trigger on separate FPGA boards
- The available latency for the Neuro Tracker is just **300ns**



 $\approx 5\mu s$ after beam crossing



The Neural Network

z-Vertex and polar emission angle prediction with a neural network:

2D track + Stereo TS $\implies z + \theta$ prediction (Current Network: One hidden layer with 81 nodes)



 \implies z-cut of $\pm 15 \,\mathrm{cm}$ used

Single-Track-Trigger: Low multiplicity trigger activated if the momentum is above 0.7 GeV (S. Bähr et al., arXiv:2402.14962)

$$p[\text{GeV}] = \frac{1}{\omega[1/\text{m}]\sin(\theta) \, 0.3 \, B[\text{T}]} \ge 0.7 \, \text{GeV}$$



Preprocessing for the L1 Neural Network Trigger







With the TS information

 $\phi_{\text{wire}}, n_{\text{wire}}, r_{\text{SL}}, \sigma_{\text{LR}}, t_{\text{d,wire}}$

we can calculate:

$$\boldsymbol{\alpha} = \arcsin\left(\frac{1}{2}\frac{r_{\rm SL}}{r_{\rm 2d}}\right)$$

$$\phi_{\rm rel} = \phi_{\rm wire} - n_{\rm wire} \cdot \left(\frac{\phi_0 - lpha}{2\pi}\right)$$

$$t_{\rm d} = \sigma_{\rm LR} \cdot (t_{
m d,wire} - t_{
m d,min})$$

Problems with the L1 Neural Network Trigger

- \bullet "Feed-Down" effect: Background tracks \rightarrow Vertex tracks
- High number of 2D track candidates from the 2DTracker when the background is high
 - \implies Many Fake-Tracks using stereo background hits







The New L1 Trigger Pipeline

• New implementation \rightarrow 3DFinder and Neuro Trigger on the same (new) FPGA board (UT4)



- $\bullet\,$ The available latency for the Neuro Tracker is increased to 700ns
- Neural networks with three or four hidden layers are possible



 $\approx 5\mu s$ after beam crossing

Extension to 3D: The 3DFinder

New curve parameter: Polar angle $\theta\implies$ 3D-Hough space

• 9 bins in $\theta \in [19, 140]^{\circ}$, 384 bins in $\phi \in [0, 360]^{\circ}$, 40 bins in $\omega \propto q \cdot p_{\rm T}^{-1}$, $p_{\rm T} \in [0.25, 10] \, {\rm GeV}/c$

Vertex assumption: The track originates from (x, y, z) = (0, 0, 0) (IP)



 \Rightarrow Intersection point yields ω , ϕ and θ



Clustering Algorithm in 3 Dimensions

Original algorithm (Sebastian Skambraks): DBSCAN Impossible to implement on an FPGA (non-deterministic length \implies latency not fixed)

Update: A new clustering option implemented in basf2: Fixed Volume Clustering

Three steps, repeated iterations times:

- Step 1: Global maximum search on Hough space
- Step 2: A fixed shape is put around the maximum
 - ▶ The weights in this shape are added up (total weight)
 - ▶ If total weight ≥ mintotalweight and peak weight ≥ minpeakweight the cluster is saved
 - ▶ All hits (TS) are extracted and have to pass two TS cuts
- Step 3: Cells around the global maximum are set to zero ("Butterfly-Shape" cutout)





Fixed shape:

Real Data Analysis: Experiment 26 Single Track Events

- Very high backgrounds were observed shortly before the long shutdown
- The Hough spaces contain a lot of fake track segments

 θ -bin 3: Exp. 26, run 1832, HLT1, f00005, event 79 (comp)





Introducing a Cut on the ADC Count

• A cut on the ADC count of the wires has been made possible by the new UT4 boards



 \Rightarrow Reduction of noise using a cut on the ADC count



FPGA Implementation

Timo Forsthofer (master's thesis, next presentation):



 \implies Cut reduction from $\pm 15 \,\mathrm{cm}$ to less than $\pm 10 \,\mathrm{cm}$ possible due to better resolution



Rejection vs. Efficiency of Different Network Architectures

Timo Forsthofer (master's thesis, next presentation):





- Deeper neural networks are considerably better than wide neural networks
- The performance is restricted by the limited input (Only 9 TS at the most)

Efficiency on Real Single Track Events



Efficiency for single track events: Cut at ± 10 cm

adccut	Efficiency 3D	Efficiency 2D
No Count 10 Counts	$94.1\%\ 96.3\%$	94.0% 95.3%

Fake-Rate for all found tracks:

adccut	Fake-Rate 3D	Fake-Rate 2D
No Count	13.1%	31.6%
10 Counts	5.8%	13.5%

But: Neural network not trained for 3D candidates at the moment (see presentation by Timo Forsthofer)

Conclusions and Next Steps

Using the 3DF inder has multiple advantages over the present 2D model with additional stereo TS selection:

- Automatic suppression of tracks outside the interaction region (candidates implicitly originate from the IP)
- Better track segment selection \implies Better resolution
- Implementation of track finding and network computation on the same FPGA board \implies Deep neural networks
- Smaller Fake-Rate
- Higher efficiency

Currently:

- Implementation of the 3D Hough method on UT4 FPGA boards (Kai Unger)
- Improved neural network architecture (Timo Forsthofer)
- Retraining with unbiased data from the new data taking







Backup

Preprocessing of the Network Input: Track Finding

Which TS belong to a real track?

TS selection using a two-dimenisonal Hough transformation:

- Axial hit in CDC (TS) gets transformed to a curve in parameter (Hough) space
- \bullet Intersection point yields the track parameters ϕ and $r_{\rm 2d} \propto p_{\rm T}$



 \implies 2D track candidate

The Neuro Trigger has been running since January 2021 years with <u>remarkable success</u>.



Background Suppression of Monte Carlo





- Promising results from Particle Gun single tracks (z_{reco} ∈ [-100, 100] cm)
- The number of found tracks falls quickly with large |z|

Parameter optimization did not sufficiently solve the following problems:

- Resource heavy on the hardware, non-deterministic length, difficult to implement
- Clusters can get very large \implies Very bad resolutions for some tracks
- Very high fake rates when using nominal phase-3 background (3 fakes for 1 signal)



Figure: The cut percentage defines how often (on average) a cell was present in the clusters.



Cluster Statistics: Average cell weights





Figure: The average cluster weights (10000 clusters) above a weight of 30: (a) Real tracks with nominal phase-3 background, (b) Fake tracks from only nominal phase-3 background.

New idea: Fixed cluster shape clustering

Monte Carlo Single-IP Tracks



- 10000 single IP tracks
- The efficiency and resolution are high with the fixed volume clustering (plot (c)):



Nominal Phase-3 Background Studies



• Very high fake rates are observed \implies Solution: Cut on the number of hit super layers



Background rate per event (10000 neutrinos):

Clustering	minhits	minsuper	N_{3d}^{all}
DBSCAN	4	0	29424
DBSCAN	6	0	11350
Fixed Volume	5	5	783

• Default DBSCAN: **290%**

• Fixed Volume with minsuper = 5 cut: 6%