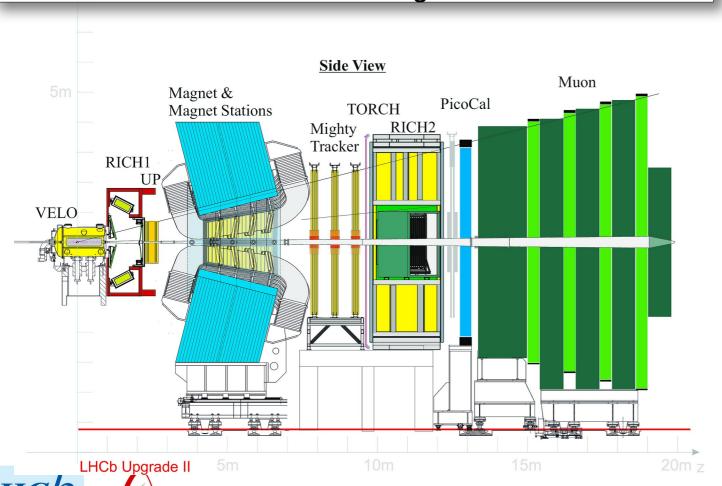


# The Upgrade II Detector (2034/2035)



Without a major upgrade to all detectors LHCb will not be able to run at these high luminosities.



- 4D tracks with <20 ps
- 1.2 × 10<sup>12</sup> tracks/cm<sup>2</sup> at r=1cm

### UT

- HVCMOS
- 9.0 Gbps

### **Magnet Stations**

new

#### MT/SciFi

Higher occupancy and radiaion

### **RICH**

- 50ps timing
- SiPM (higher resolution)

### **TORCH**

new (25ps timing layer)

### ECAL, HCAL

- SpaCal+Shashlik
- time + space resolution

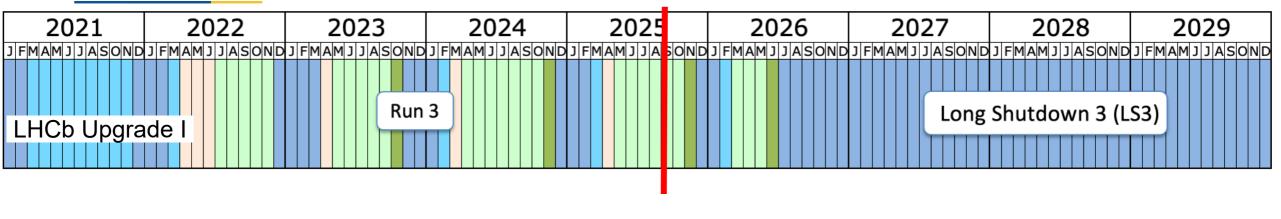
### **MUON Stations**

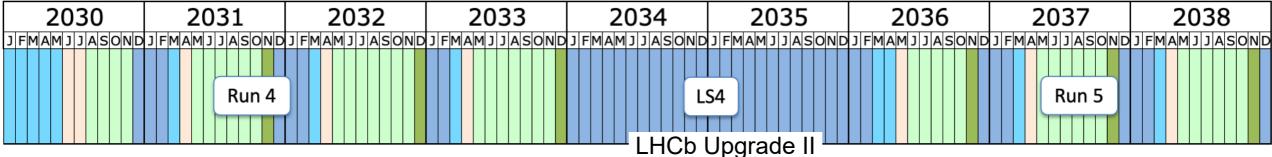
- higher granularity
- maybe shielding

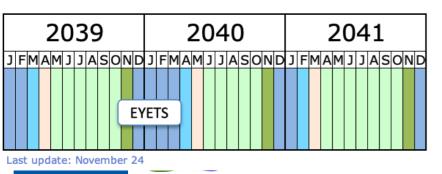


### Timeline

### Now





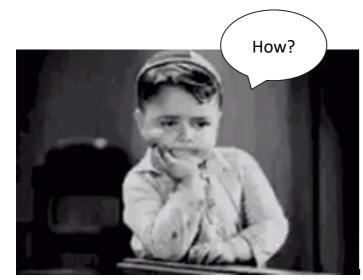


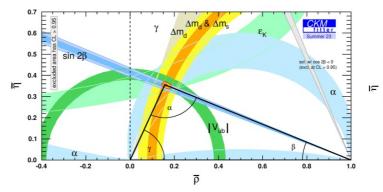


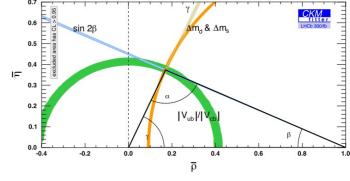


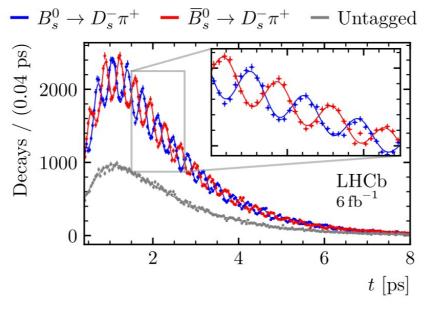
# Goal of the Upgrade

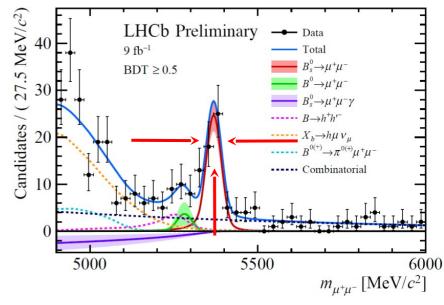
- More statistics
- Higher Precision
- → Better Physics Results











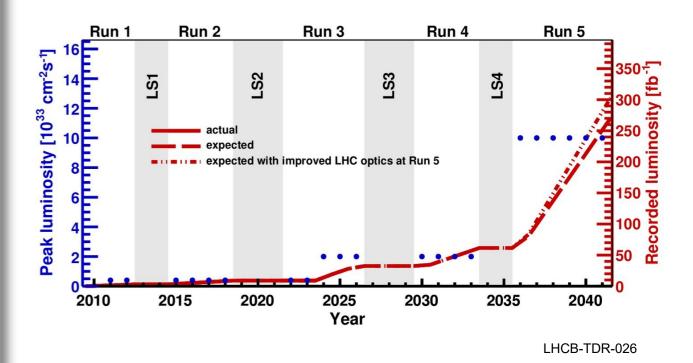




## The Upgrade II Conditions (2034)



Run 3-4	Run 5-6
2 × 10 <sup>33</sup> cm <sup>-2</sup> s <sup>-1</sup>	1.0 × 10 <sup>34</sup> cm <sup>-2</sup> s <sup>-1</sup>
50 fb <sup>-1</sup>	250 fb <sup>-1</sup>
<µ>=7	<µ>=50
radiation hard	Current detectors will not work in this radiation enviroment
Streamless Readout	
acceptance η=1.9 to 5	

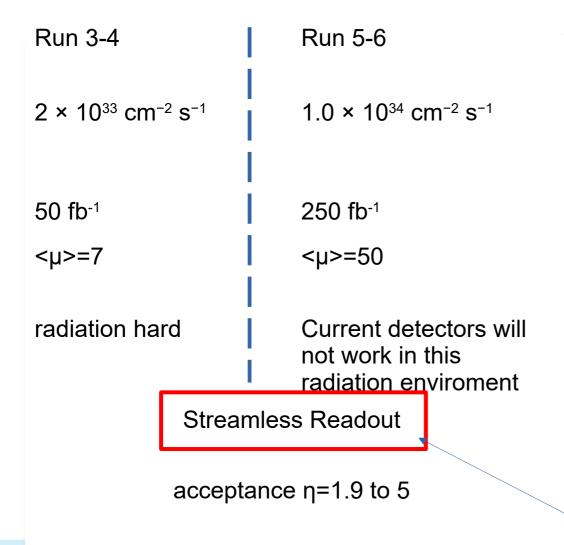


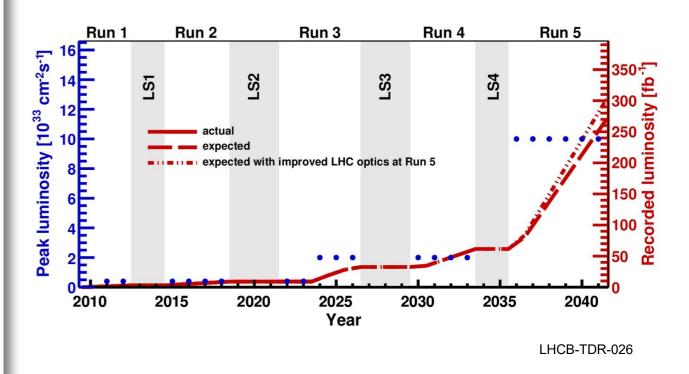




### The Upgrade II Conditions (2034)







Untriggered readout @40MHz



## Streaming Readout

LHCb uses no Hardware Trigger

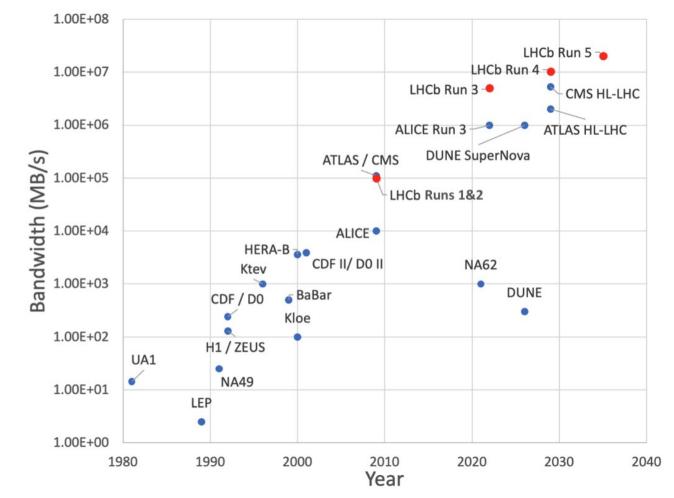
All data is processed and reconstructed

LHCb is the experiment with the worlds highest bandwidth output!

LHCb now is at the level of the coming ATLAS and CMS upgrades ~O(TB/s)

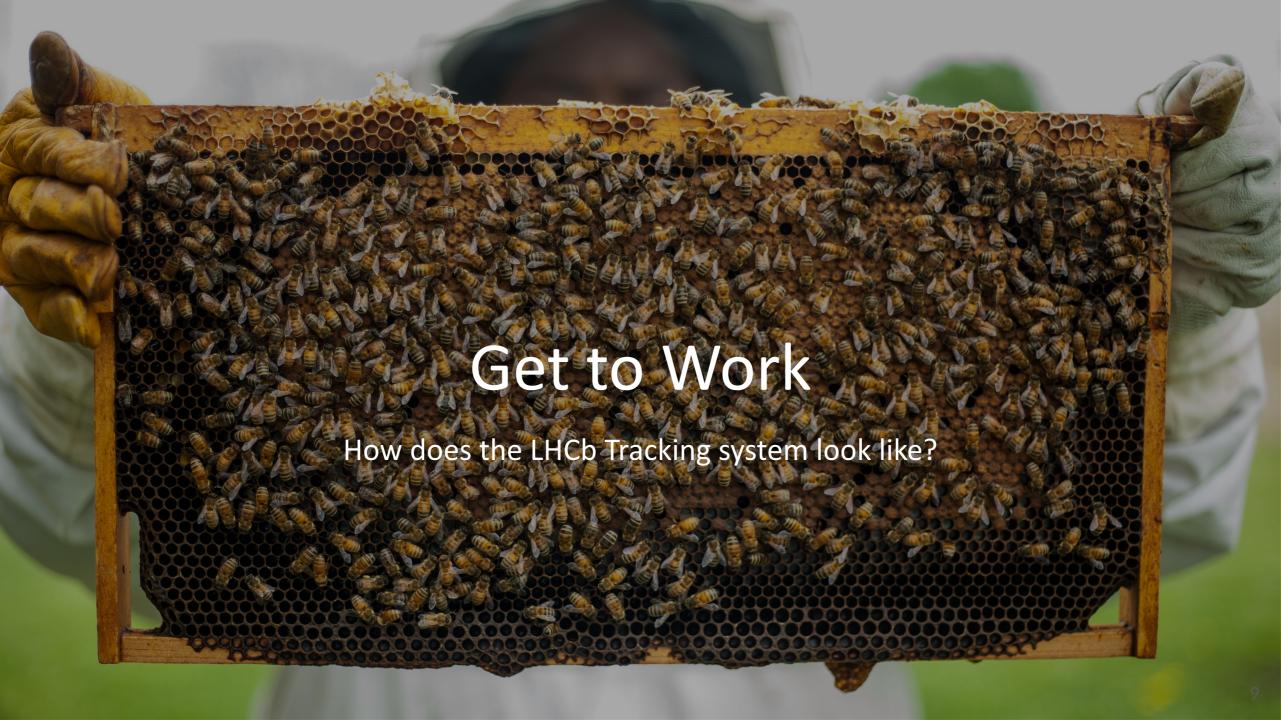
We will have to 10TB/s in LS3 (new RICH and ECAL

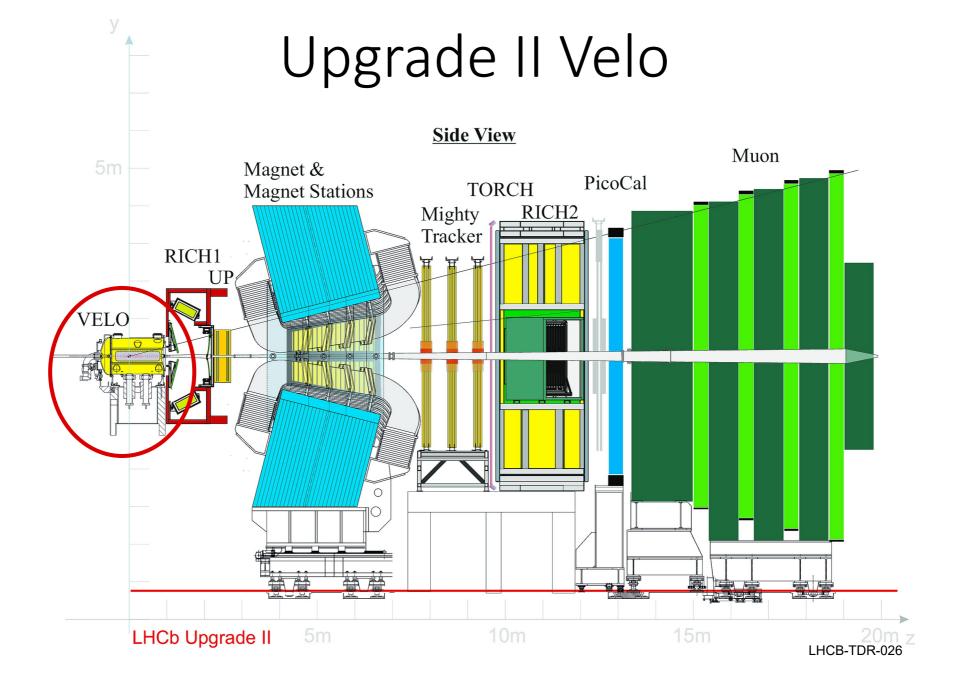
For Upgrade II in LS4 we expect a Bandwidth requirement of 25TB/s





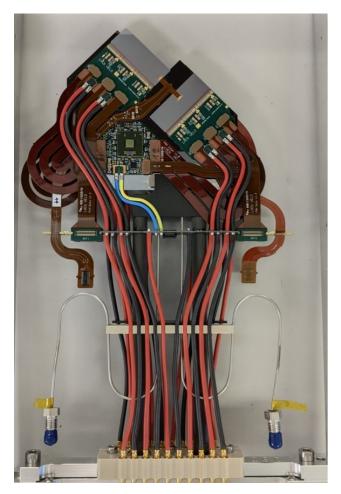




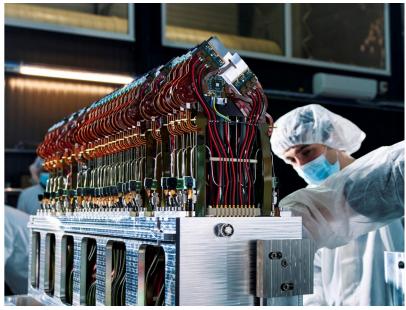


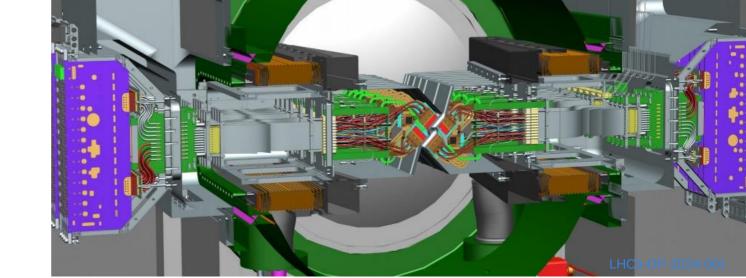


### Current Velo



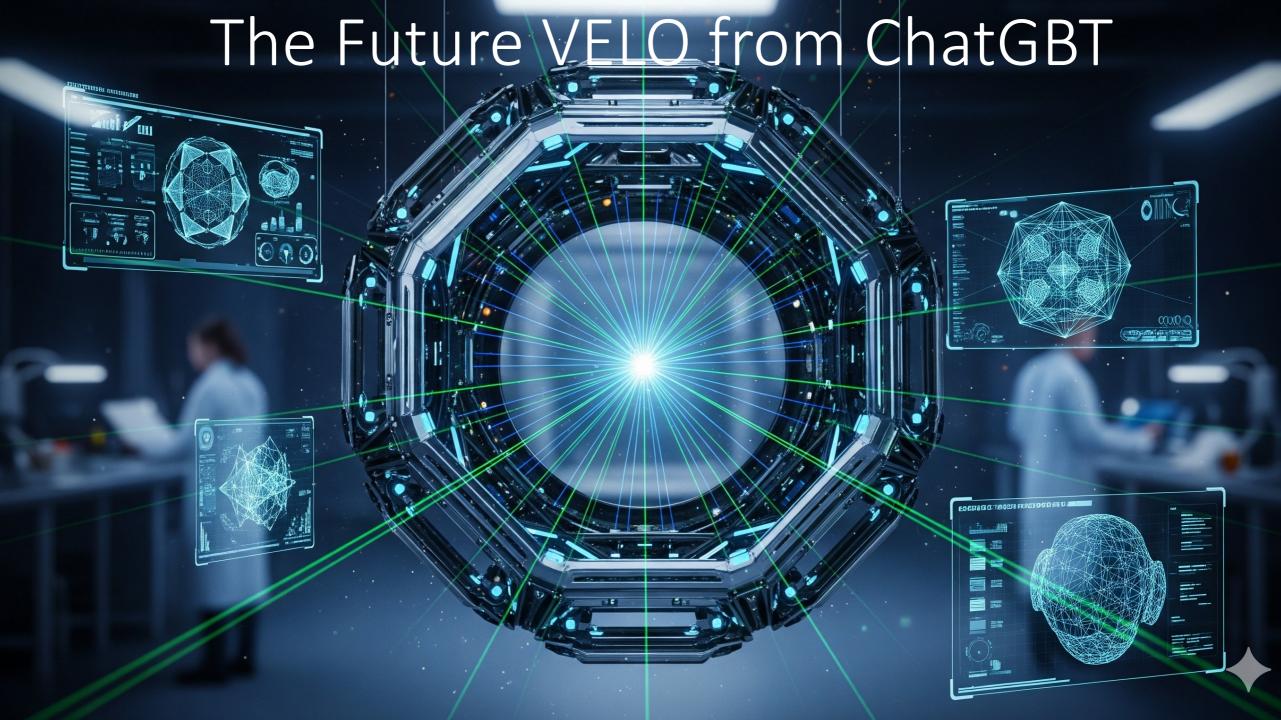
- L-shaped sensor arrangement
- Readout close to sensor
- As close to IP as possible (moves in)
- Primary and secondary vacuum (<10 mbar)</li>
- 52 modules (0.12 m<sup>2</sup>)
- 55µm pixel pitch
- 250μm (180μm) RF Foil
- Micro-Channel CO<sub>2</sub> cooling













### **VELO** Optimization

**Extrapolation Term Scattering Term 20**μm **250**μm pixel pitch pixel pitch cylindrical corrugated 55µm no foil foil 40µm foil 14.9\*10<sup>15</sup> 월 16 Oscat = 2.5 x Upgrade 1 0.6\*10<sup>15</sup>

1\*10<sup>15</sup>

Since the delivered luminosity of 50 *fb*<sup>-1</sup> in 1 MeV 15 14 Oscar = 2 x Upgrade 1 Inner Radius Sensor [mm] 13 Scenario B Scenario B 12 1\*U1 Fluence Oscar 11.5 x Upgrade 1 OScat 10 0 2\*U1 Fluence Os (1) Upgrade 1 \* Upgrade 6\*U1 Fluence Scenario A Scenario A 10 12 14 16 0.0 1.0 1.5 3.0 2.0 Radiation Length (up to the second hit) [%] Hit Resolution [μm] LHCb-PUB-2022-001





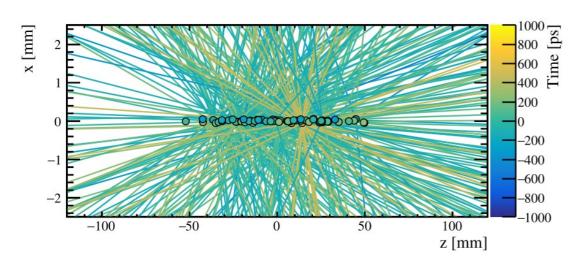
### Velo Upgrade

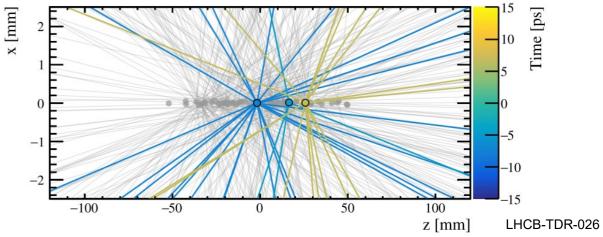


 $r_{in}^{max} = 10.18 \text{ mm}$ 

800

z (mm)





400

600

### Keep 10µm impact parameter resolution:

- 28 nm Hybrid ASIC
- 50 ps time timestamps per hit
- 50µm pixel pitch 3D sensor
- 94-250Gbit/s (B/A) output for the hottest ASIC
- NIEL:  $10^{16}$  1MeV  $n_{eq}$ /cm<sup>2</sup>
- TID: 400 MRad

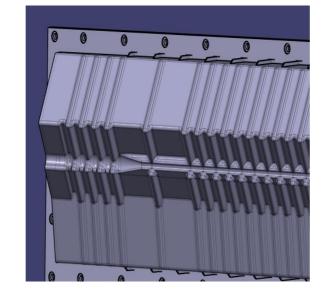








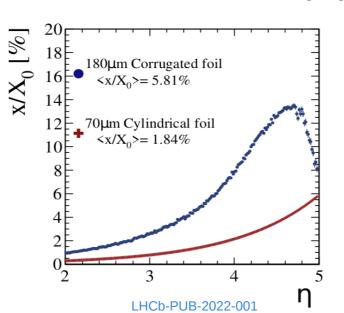
### **RF** Foil

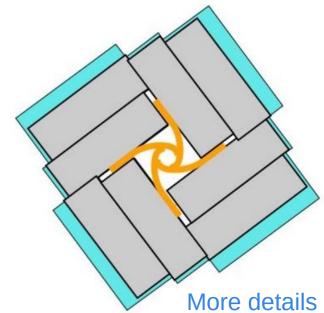


- Current RF foil corrugated design
- Probably will be pushed to a more cylindrical profile
- Pushes the material away from the beam where possible
- Foil 70μm (Al) electrically minimum (75μm baseline)

Box or Iris closing is also studied

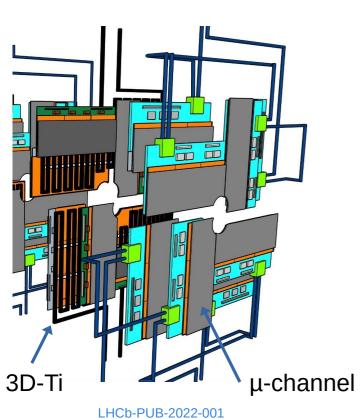




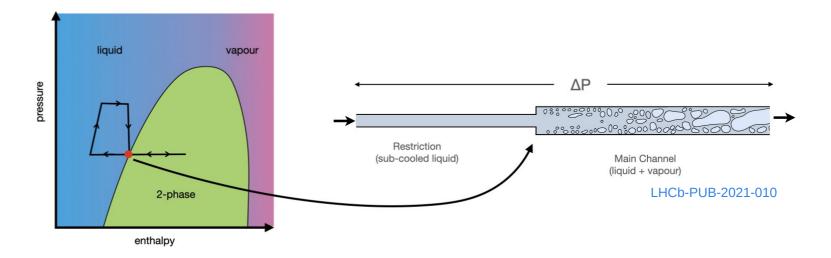


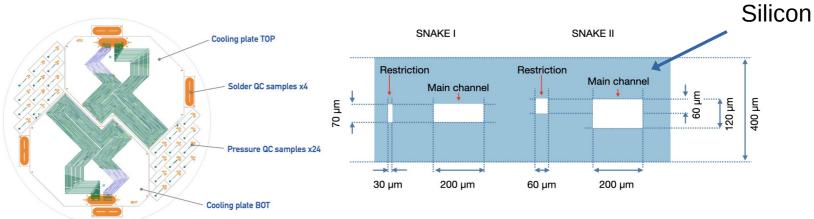


## Cooling



# So far $\mu$ -channel CO2 cooling was used also 3D printed Ti was considered





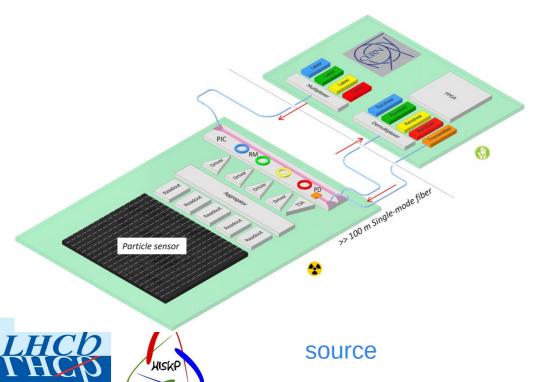


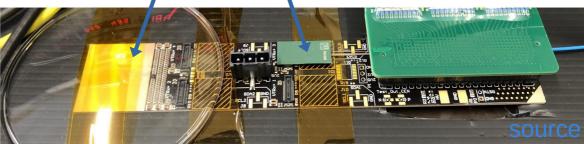




### Data output

- Need to go to about 100-250Gb/s per ASIC
- Dircet to VTRx+
- Silicon photonics



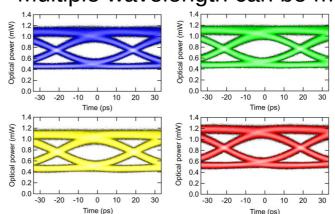


VTRx+

VTRx+ is shown to be radiation hard (few 100MRad) 10.24 Gb/s per VTRx+ (will not support 10 VTRx+ per module)

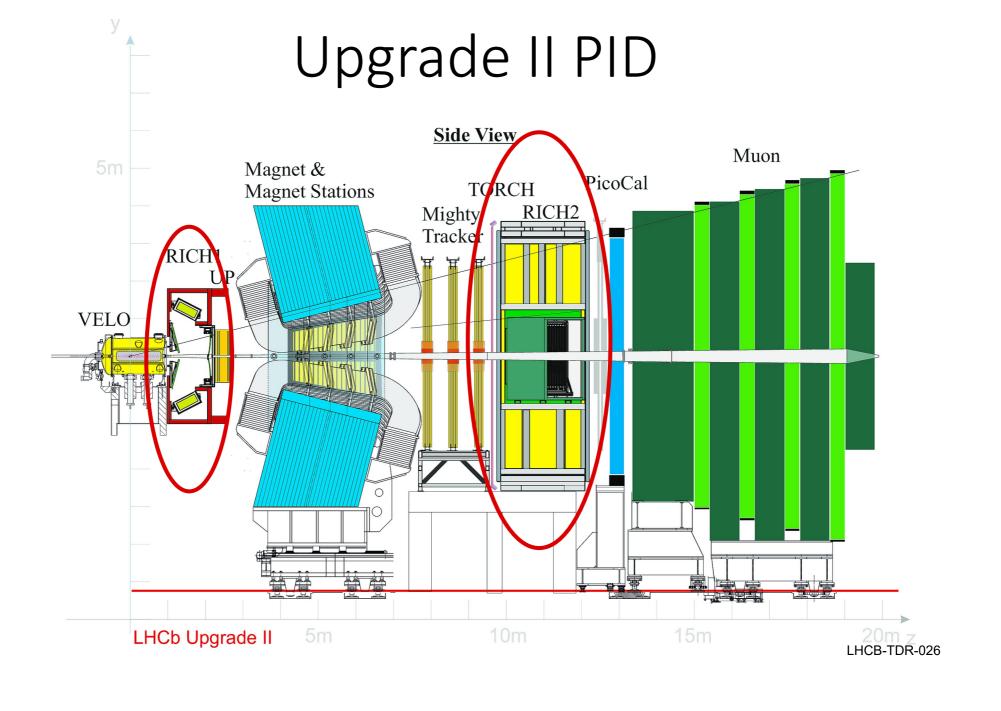
### Silicon Photonics:

Integrate the optical output into the ASIC
Use external laser source
Change the light output by ring modulators
Multiple wavelength can be modulated from the same fiber



Timepix4

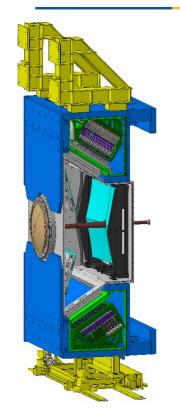
Shown to work for 25Gb/s per modulator



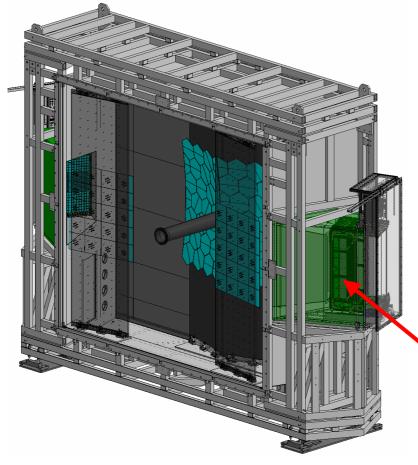




### RICH Upgrade

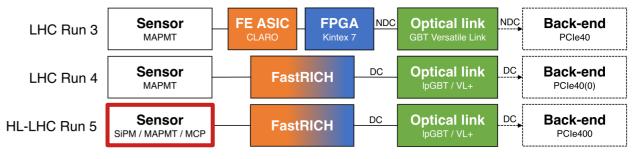


RICH 1 3- 65 GeV PID



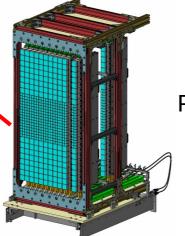
RICH 2 15-10 GeV PID

### Main upgrade: Readout and Sensors



LHCB-TDR-026

- 100 ps photon resolution (FastRICH electronics)
- PMT → SiPM or MCP-PMT (higher granularity)



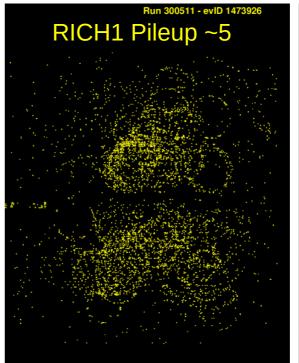
Planned Rich2 readout

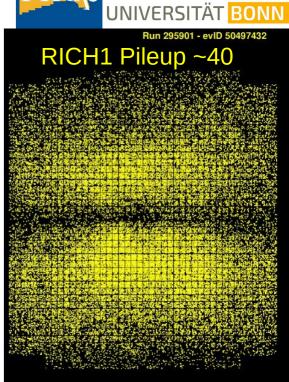


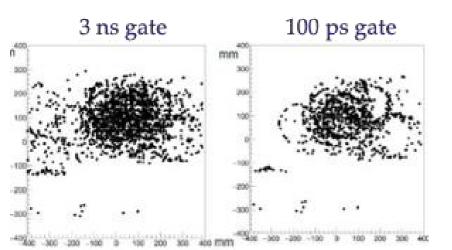


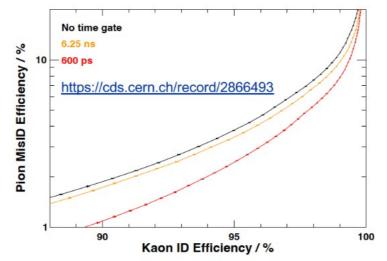
## Challenges for PID

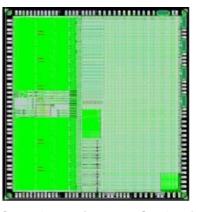
- Need to reduce the number of photons to reconstruct Cherenkov rings
- FastRICH ASIC can deliver 25ps timestamps
- Other mirror configurations under investigation
- SiPM or MCP under investiation
  - Cryogenic cooling for SiPMs planned
  - MCP aging issues under development



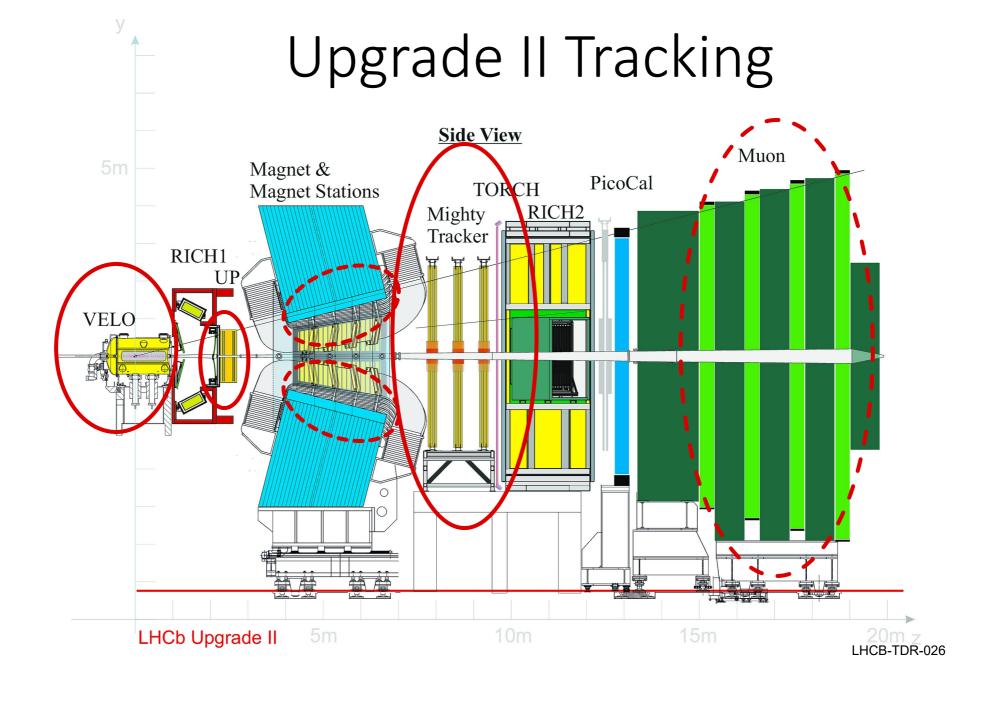






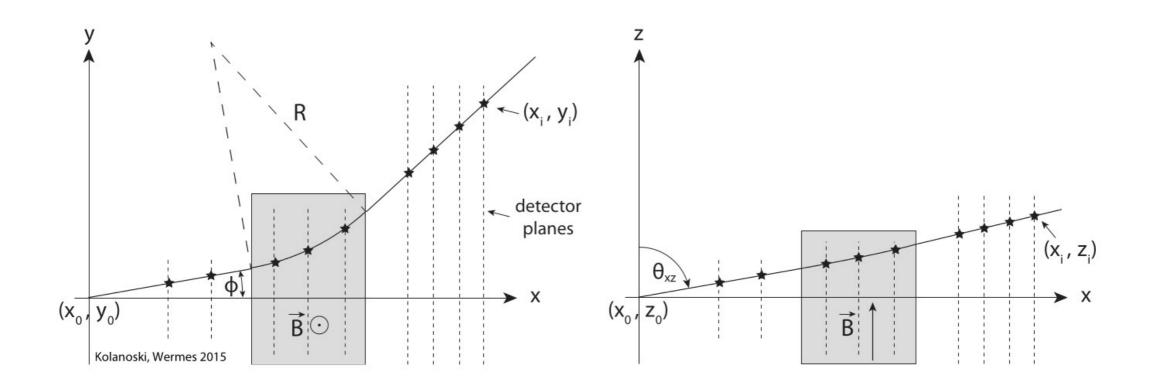


Fast RICH ASIC (65 nm technology)
Fast RICH ASIC in the lab since June





## Tracking in LHCb



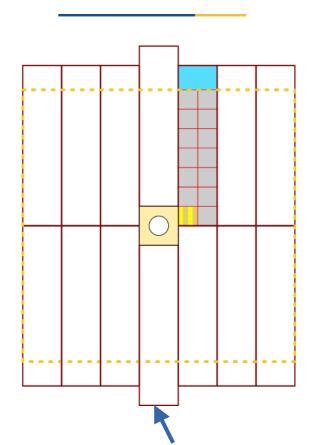








## Upstream Pixel

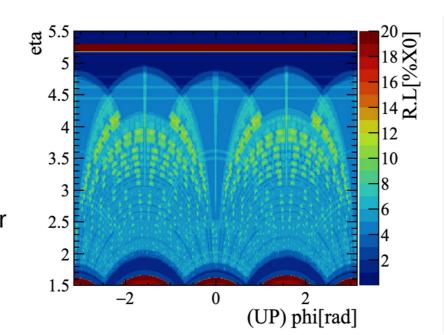


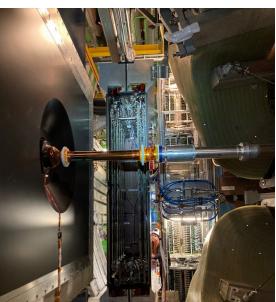
- See why this design later

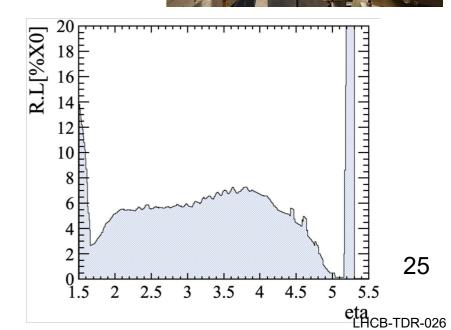


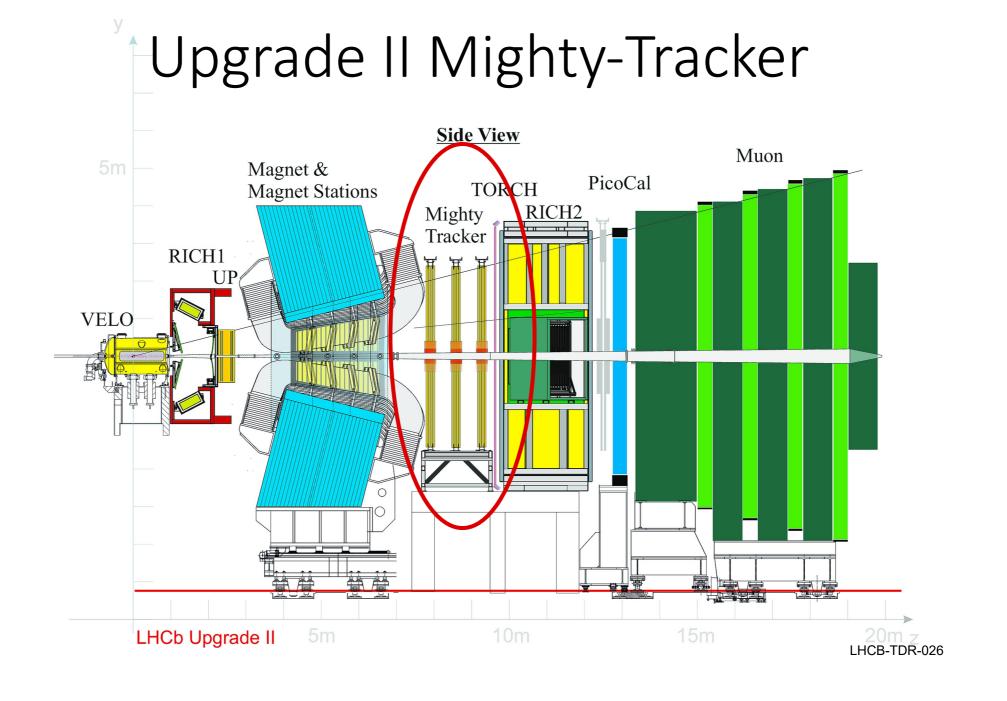
- 4 Layers
- Monolithic Sensors
- 9 m<sup>2</sup> active area
- 74 (34) MHz/cm<sup>2</sup> at 4cm (6cm) from beampipe
- NIEL: 3 10<sup>15</sup> 1MeV n<sub>eq</sub>/cm<sup>2</sup>
- TID: 240 MRad

Planned to use the same sensor as Mighty-Pixel and probably similar module design







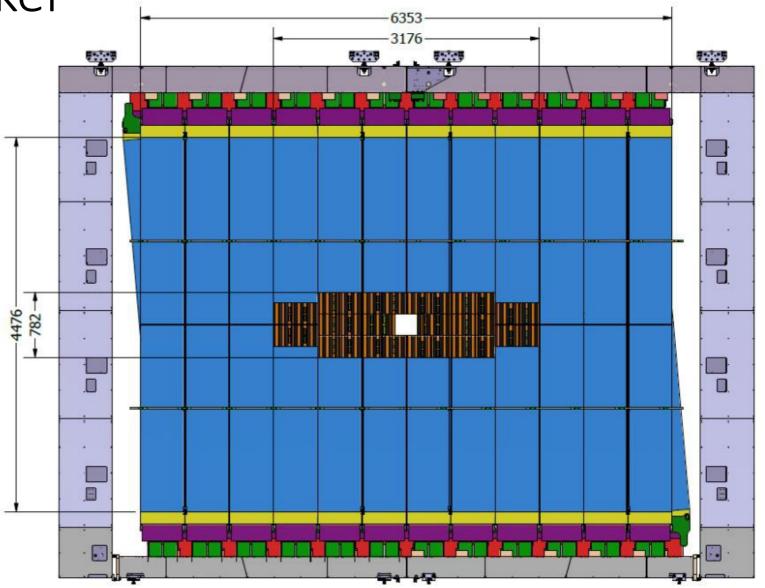






The Mighty Tracker

- Mighty SciFi tracker for the outside 12 layers in 3 stations (170m² effective)
- Mighty Pixel for the high occupancy regions in 6 layers (9 m²)



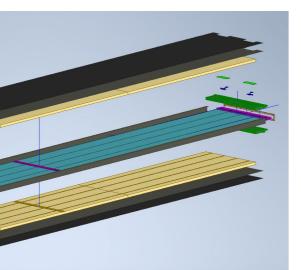


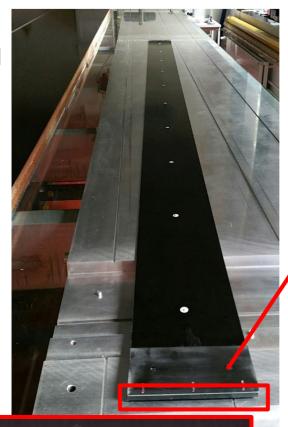




# SciFi Working Principle (Current SciFi)

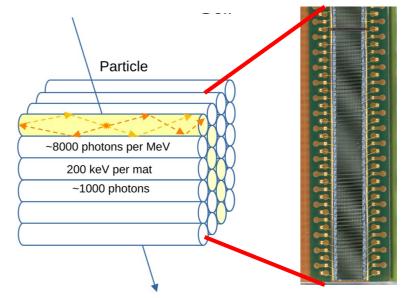
Module: Long fibers Minimal support material







- Very successful application to instrument a large area
- Low material budget  $(X/X_0 \le 1\%)$  per detector layer)
- XUVX layout (±5° for U and V)
- Main complexity to produce homogeneous fiber mats
- 70 µm resolution

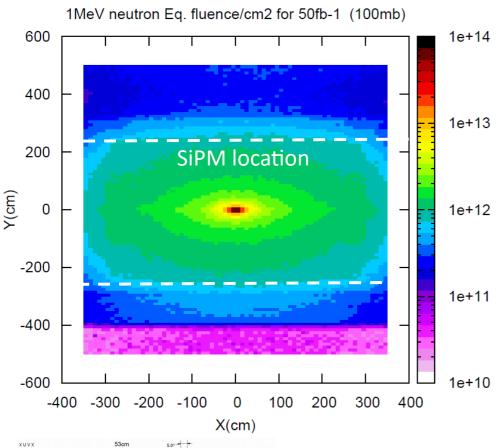








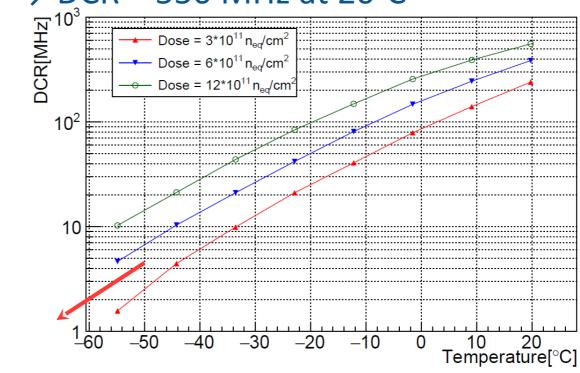
## SiPM Challenges



### **Unirradiated SiPM:**

Dark Count Rate (DCR)  $\sim 0.04$ MHz Expect  $6.10^{11}$   $n_{eq}$ /cm<sup>2</sup>

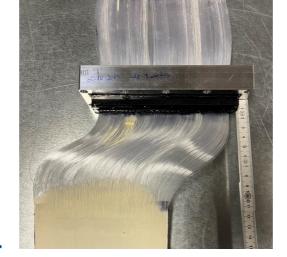
→ DCR ~ 550 MHz at 20°C

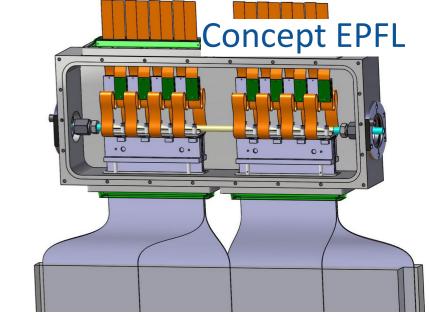


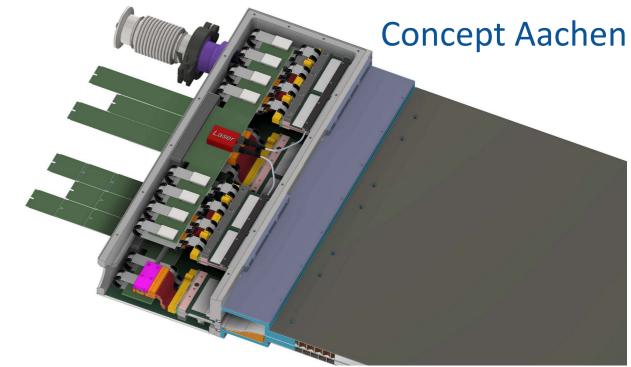


## Cryogenic SciFi

- Goal is a "noise free" detector
- Challenges: cryogenic cooling for a larger tracking detector
- Fiber bending at the end
- Thermal insulation of the coldbox
- Use liquid N<sub>2</sub>
- About 3 kW cooling power needed
- First test show great promise





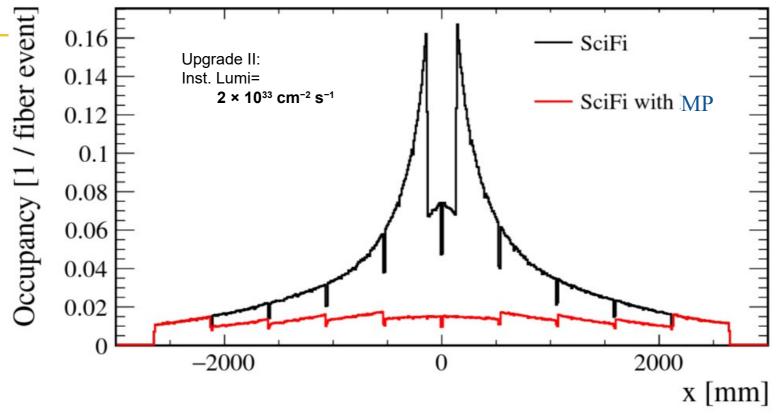






## Why do we need the Mighty-Pixel





LHCB-TDR-023

The number of hits/fiber/event is too high to allow efficient tracking Need to have a higher granularity in the central region.

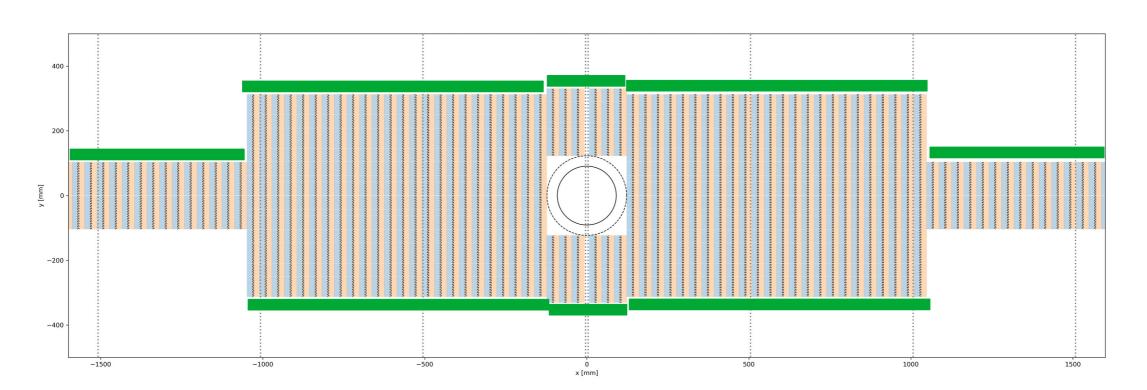






## Mechanical Layout

One Layer 1.49m<sup>2</sup>
9m<sup>2</sup> in total
2 Layers per station
6 Layers in total



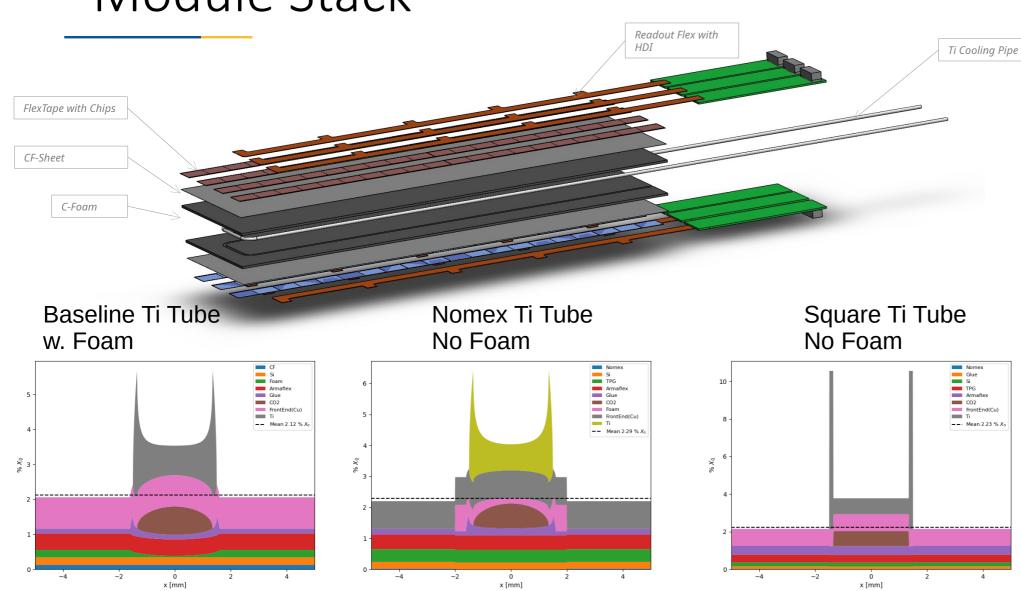


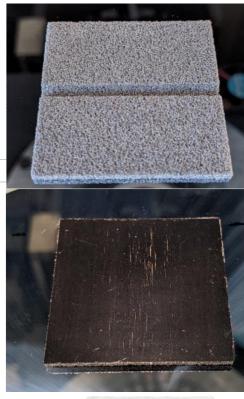




- Shape follows the highest acceptable occupancy in the fiber region
- Readout is moved out as far as possible from the hottest region

### Module Stack



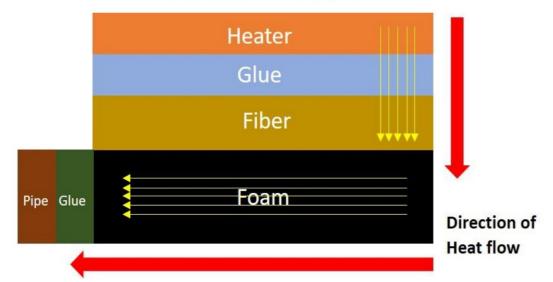


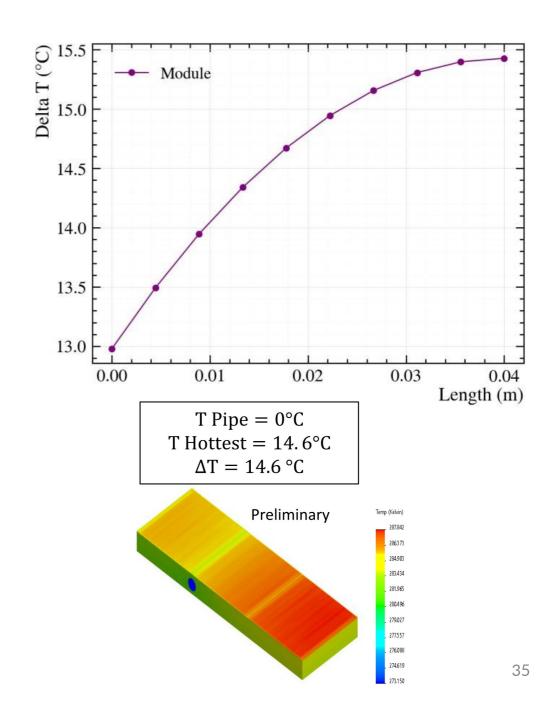


## Cooling

- Baseline two phase CO<sub>2</sub>
- The natural detector layout favours a colder beamhole area
- For -10°C at chip → -25°C coolant
- Sensor power dictates the number of cooling lines

 $Heat load = 0.3W/cm^2$ 







## MightyPix 1

There is a problem with the configuration of the MP1

→ could be roughly fixed by FIB surgery

The time binning is rough

4.74 ns measured  $\rightarrow$  ~ 2.9 ns intrinsic uncertainty

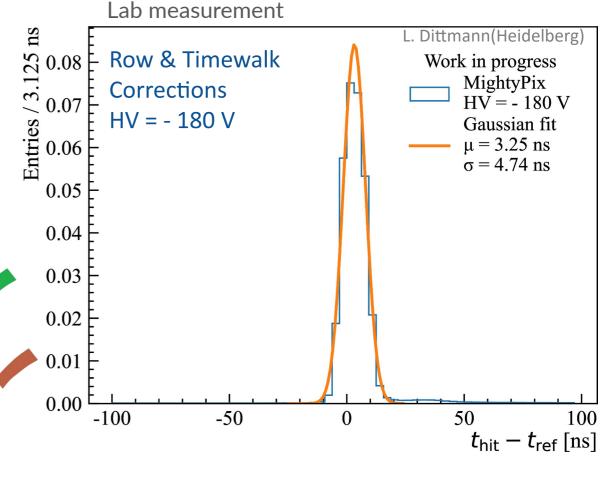
Power consumption 190-250 mW/cm<sup>2</sup> (scaled to full chip)

Due to chip repair we can not use this chip in the testbeam

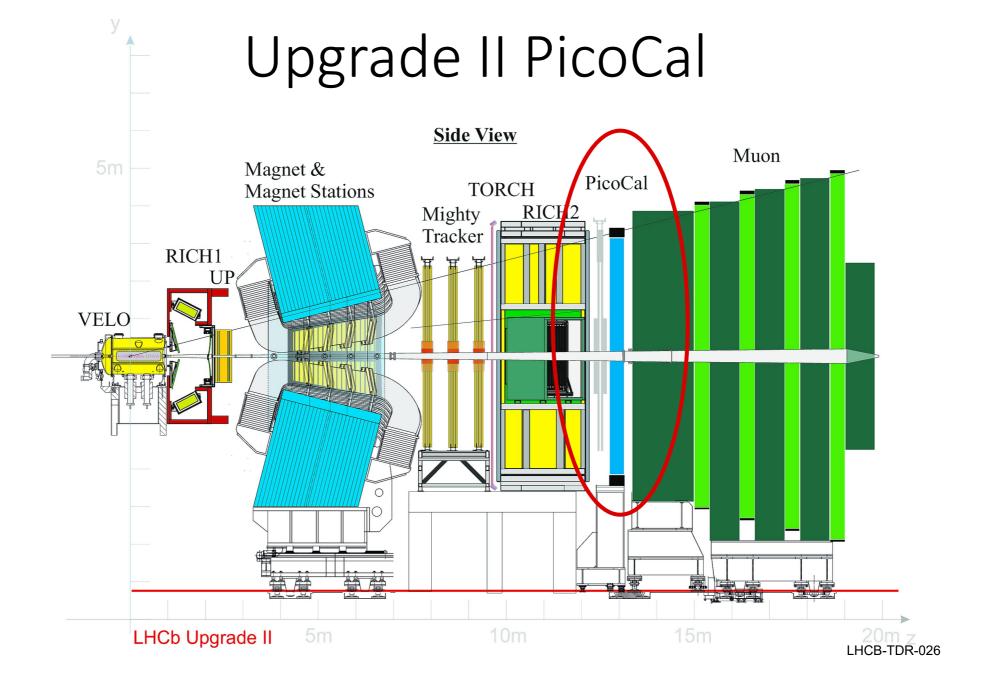
 $\rightarrow$  Same analog pixel design in TelePix 2 (½ the pixel size)







Many Thanks to our colleagues from DESY, KIT and Heidelberg we are validating this with the TelePix 2



# Interesting... The PicoCal



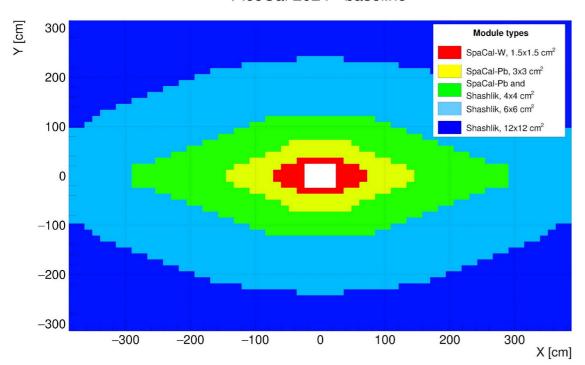




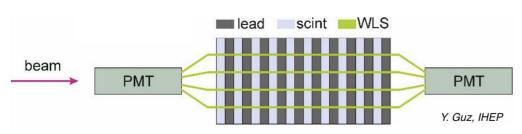


## PicoCal





#### Shashlik



#### Readout follows the occupancy Adopt technologies to cover full area

#### **SpaCal** technology for inner region

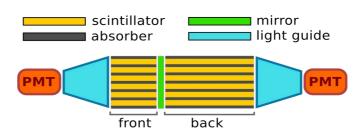
- 1.5 x 1.5 cm<sup>2</sup> cell W absorber + crystal fibres
- Development of radiation-hard crystal fibres
- Polystyrene fibres for Run 4, then replaced by crystals
- $3 \times 3$ ,  $4 \times 4$  cm<sup>2</sup> cell Pb absorber + plastic fibres:
- Need radiation-tolerant plastic fibres

#### Shashlik technology for outer region

4x4, 6x6, 12x12 cm<sup>2</sup> cell

 Timing improved with faster WLS fibres and doublesided readout

#### SpaCal



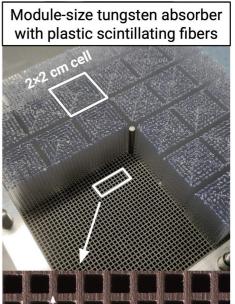
### PicoCal Status of R&D

- Tungsten Absorber: successful prototype 3D printed
- Lead absorber: Low pressure casting
- Encouraging energy resolution:
  - Sampling term: 11.2%
  - Constant term: 1.2%
- Time resolution < 20ps above 20GeV

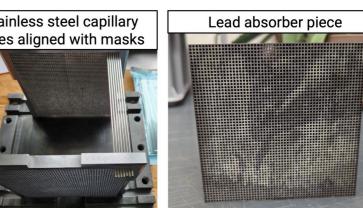
#### Fiber:

- Found organic scintillators that are promising
- Crystal scintillators under investigation

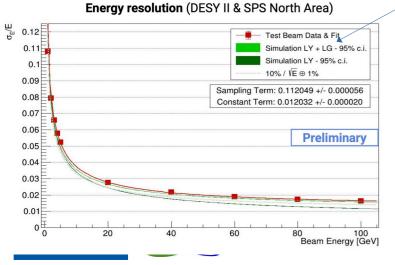




#### **Further Information**

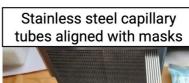


#### SpaCal-Pb single module



#### SpaCal-Pb double-sided Time resolution (DESY II & SPS North Area)

DESY - Back DESY - Weighted Average SPS - Back SPS - Weighted Average LHCb-TDR-024 20 ps Beam Energy [GeV]

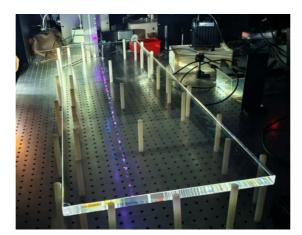




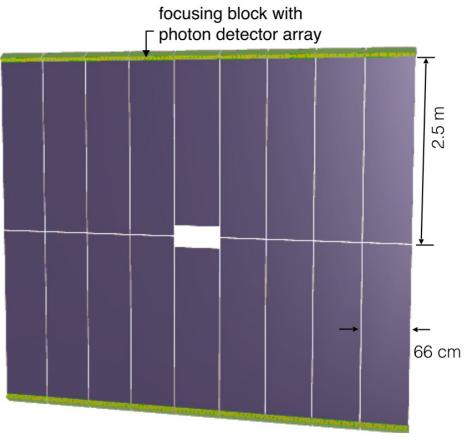
### **TORCH**

- Focusing Block

  Focal Plane  $\theta_z = 0.45 \text{ rad}$   $\theta_z = 0.85 \text{ rad}$
- Provide time information for the RICH2
- Cherenkov light passed transported via total reflection
- 25ps time binning
- Use FastRich Readout
- Can use Microchannel-plate-PMTs may use SiPMs (to use the same technology as RICH)
- Challenges:
  - Space in cavern
  - Flatness of fused silica
  - Occupancy







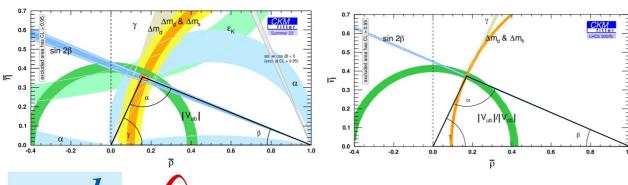
Surface measurement of large fused silica plate

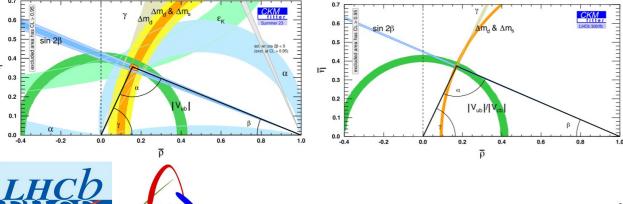


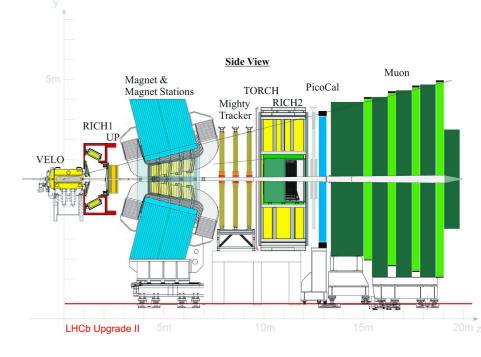


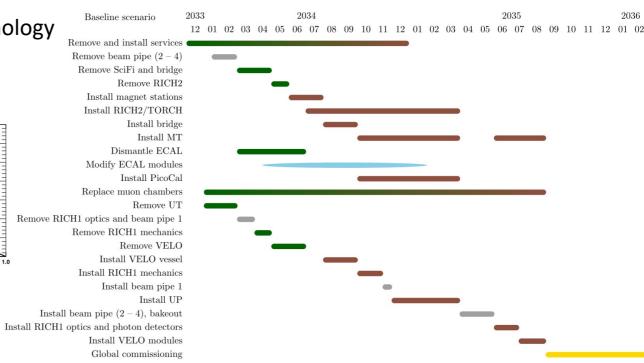
# Summary

- Exciting times ahead of us
- 9 years to go
- Start production in 4 years
- Just 1 year to install a fully new detector
- Upgrade II will push the frontier of detector technology





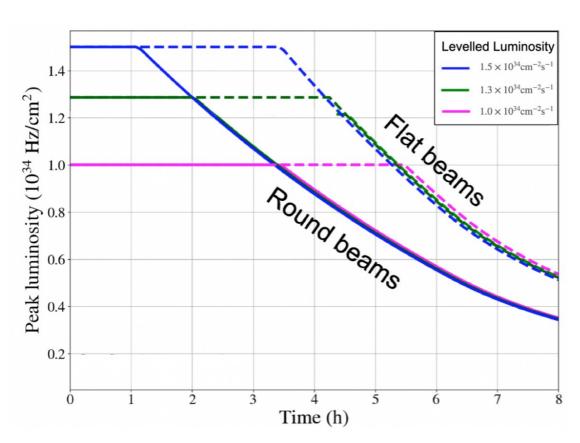








# Luminosity



	Round optics		Flat optics	
Levelled $\mathcal{L}_{\text{peak}} (10^{34}  \text{cm}^{-2}  \text{s}^{-1})$	1.0	1.5	1.0	1.5
$\beta_x^*/\beta_y^*$ (m)	1.5/1.5		0.5/1.5	
$N_{\rm bunch}$ total/colliding in LHCb	2760/2574		2760/2574	
Levelled pile-up	28	42	28	42
Delivered $\mathcal{L}_{\mathrm{int}}$ per year (fb <sup>-1</sup> )	42.16	49.34	48.73	63.36
Levelling time $t_{\text{lev}}$ (h)	3.42	1.08	5.42	3.42
Optimal fill length $t_{\text{opt}}$ (h)	7.67	7.58	7.58	7.42
$t_{ m lev}/t_{ m opt}$	0.45	0.14	0.72	0.46
RMS luminous region $(z)$ at $t = 0$ (mm)	43.30	43.31	38.41	38.45
Peak pile-up density at $t = 0 \text{ (mm}^{-1})$	0.29	0.40	0.41	0.54

LHCB-TDR-026



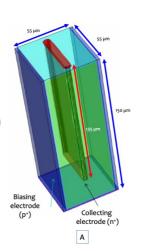


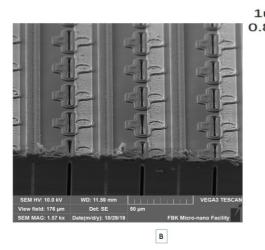


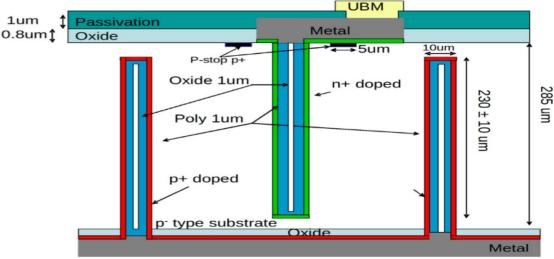
### Velo Sensor

3D sensor (trench or column):

- Radiation tolerance up to  $10^{16}$  MeV  $n_{eq}$
- Difficult to produce
- 10ps resolution per pixel possible



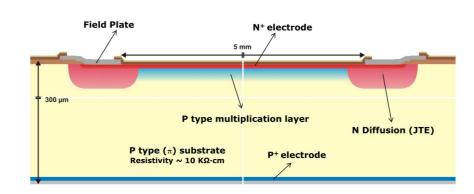




#### source

#### **LGAD**

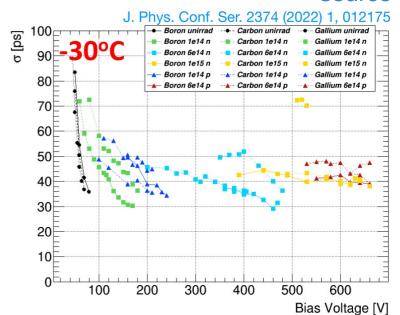
- Time resolution ~40ps
- Radiation tolerance\*
   ~3-4 10<sup>15</sup> MeV n<sub>eq</sub>







\*radiation tolerance is under study and probably can be improved



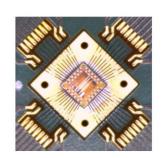


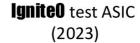
## Velo ASIC

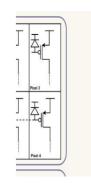
Feature	IGNITE (3D Integration)	LA-PicoPix (Per-Pixel Compensation)	
Power Management	Split Analog & Digital ASICs for uniform power distribution	On-pixel power compensation to correct local fluctuations	
Pixel Size	Smaller pixels (~45 µm) due to separate processing layers	Larger pixels (~50 µm) but single ASIC architecture	
Post-Processing	Requires 3D integration + TSV (Through-Silicon Vias)	No TSV needed → Simpler data processing	
Scalability	Potential for higher density integration	Easier manufacturing & integration	

#### Two developments ongoing:

- IGNITE (TimeSPOT development)
- PicoPix (Timepix4 development)







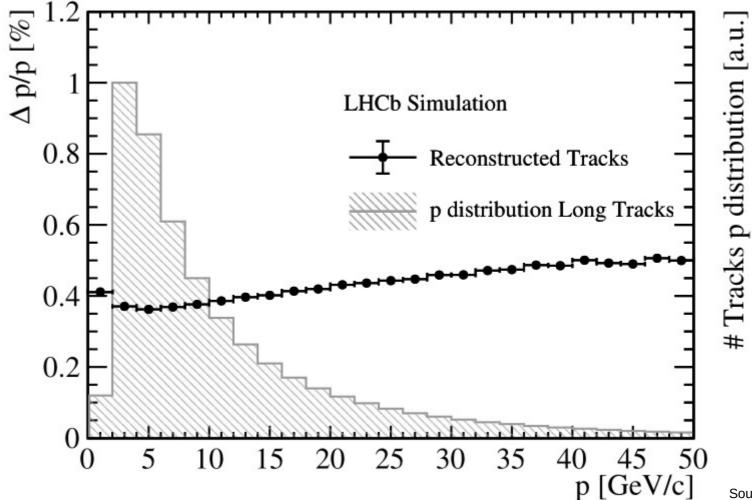
PicoPix analog pixel cell







# Tracking in LHCb





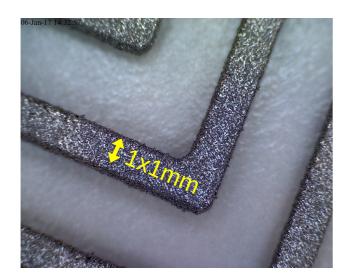


Source P. A. Günther (2023)



# 3D Titanium

- Fully developed for current Velo
- More flexible for various pressure drops
- More X<sub>0</sub>





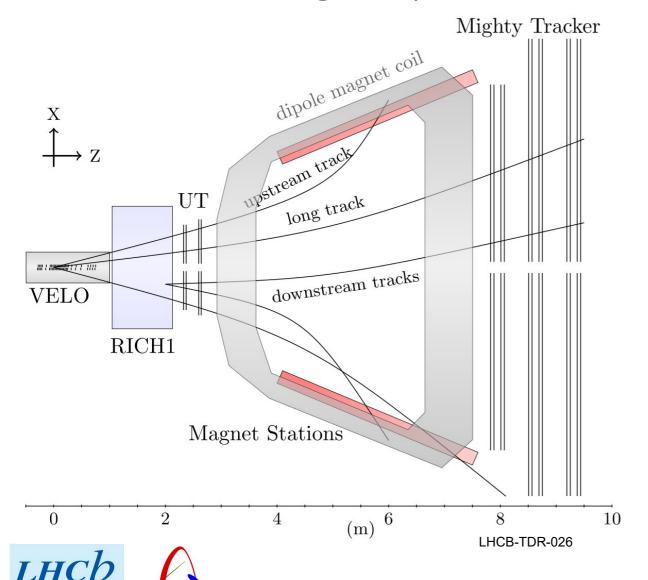






# LHCb Tracking Requirements





- reconstruction of hadron decays
  - many final state particles
  - high pointing precision
- the highest efficiency possible
  - → many layers
- long lever arm
  - → limited by the cavern
- high resolution
- low material budget

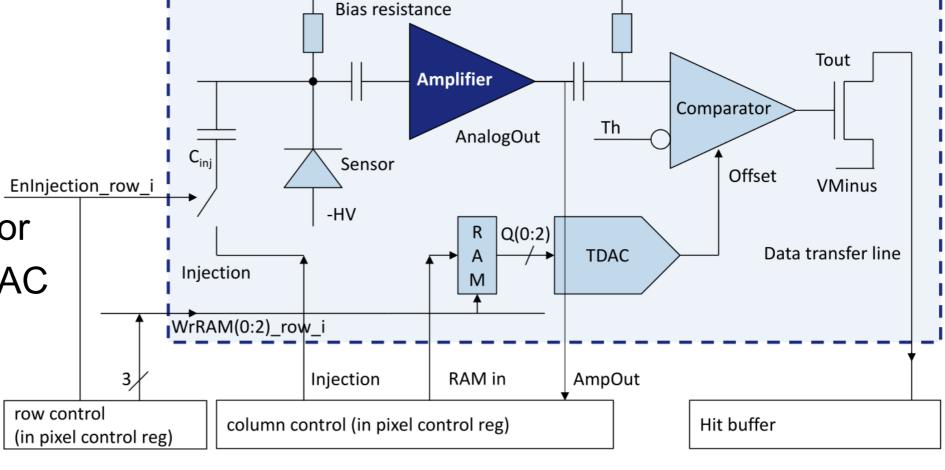


BL

Mighty Pixel Analogue Part

### In Pixel:

- Sensor diode
- CSA amplifier
- CMOS Comparator
- Threshold tune DAC
- RAM for tune bits







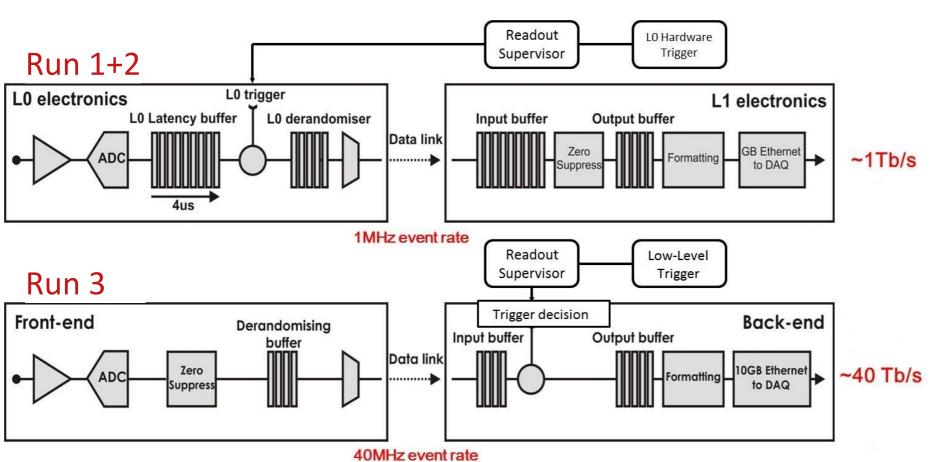
Source: Ivan Perić

1.8V

Pixel i



# LHCb Upgade I → "Triggerless" readout



With a later High level Trigger more relevant data can be recorded!



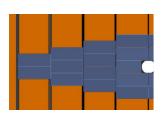


F Alessio 2013 JINST 8 C12019



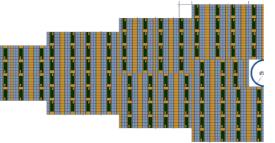
# A Short History of the Mighty

18m² Some Silicon area



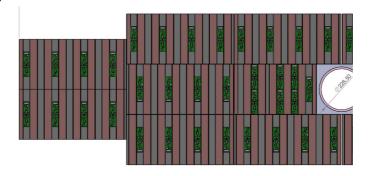
Fully integrate silicon into the fiber planes

18m²
FTDR
detailed ideas for readout



Separate silicon and fibers
Ohh and we have to be cold

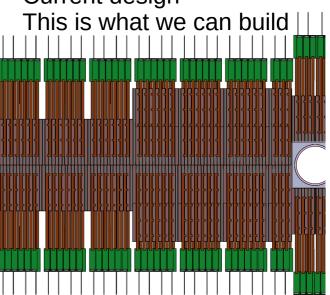
13m²/8m² Scoping document This is what we can afford



Support structures are designed and clear budget



9m<sup>2</sup> Current design



Cooling solutions and auxiliary electronics are realistic



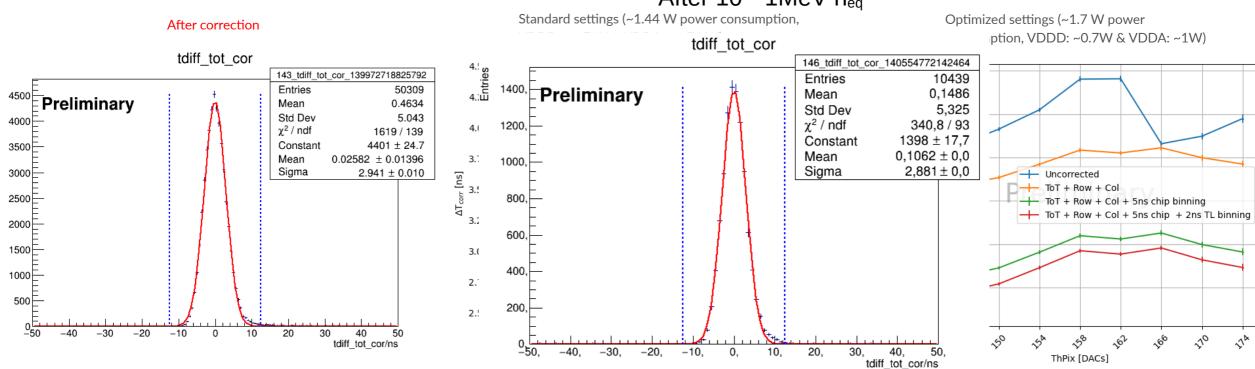




### Time Resolution - Results

#### After 10<sup>14</sup> 1MeV n<sub>eq</sub>

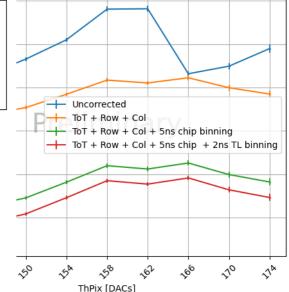
Fit fails if time walk is too large



- Time resolution highly threshold dependent
- → No plateau
- Compatible with testbeam  $\rightarrow$  ~3 ns w/o binning correction







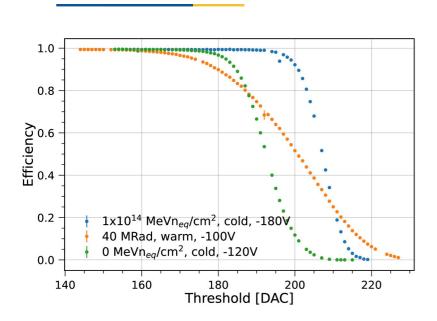
N. Sommerfeld and H. Schmitz

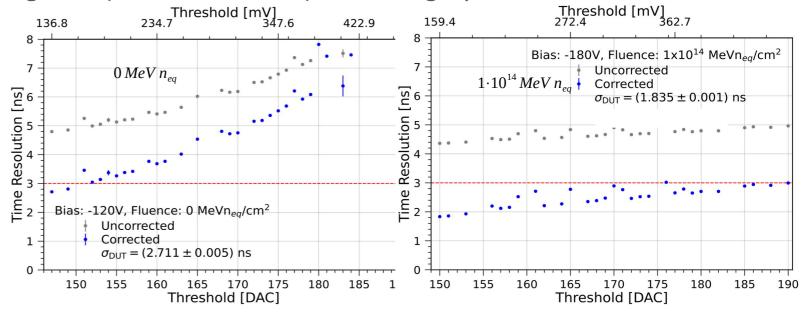
#### TelePix2 Paper



# TelePix2

- Sensor designed for DESY testbeam as telescope sensor
- Same analog Pixel (with half the size) as the MightyPix1





H. Schmitz and N. Sommerfeld

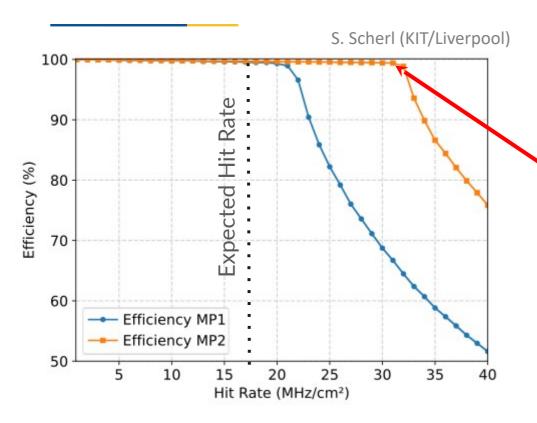
- Testbeam could show that sensor is radiation tolerant
- TID softens the slope
- Second TelePix sensor as time reference







# Hit Rate Simulation MightyPixel

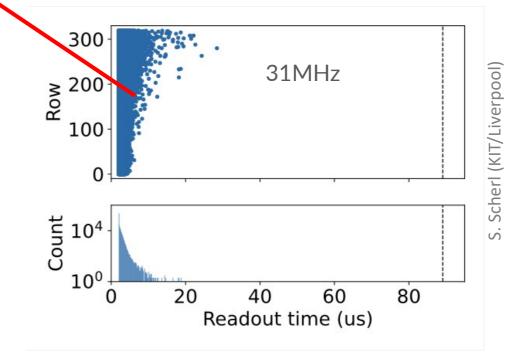


Expect a Hit rate up to 17 MHz/cm<sup>2</sup>

MightyPix 1 should work up to 20 MHz/cm<sup>2</sup>

MightyPix 2 will work up to 32 Mhz/cm<sup>2</sup>

Probably further reduction possible with less data

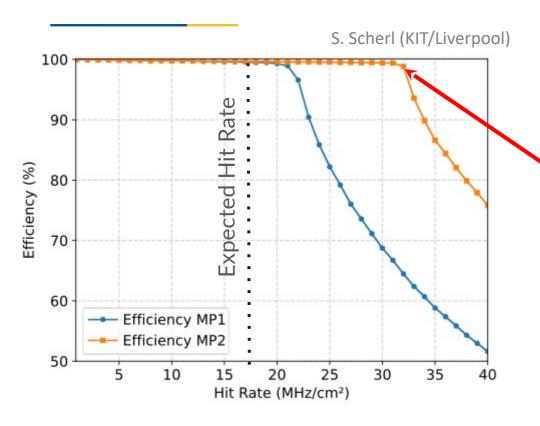








# Hit Rate Simulation MightyPixel

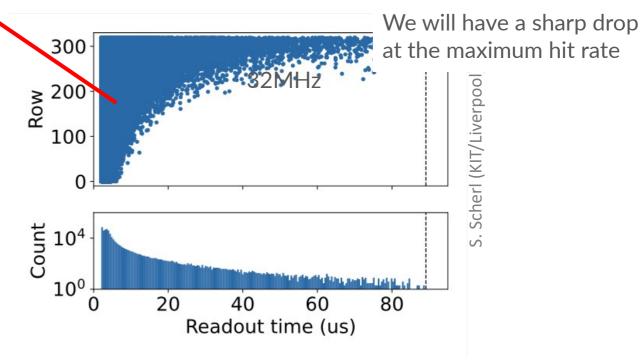


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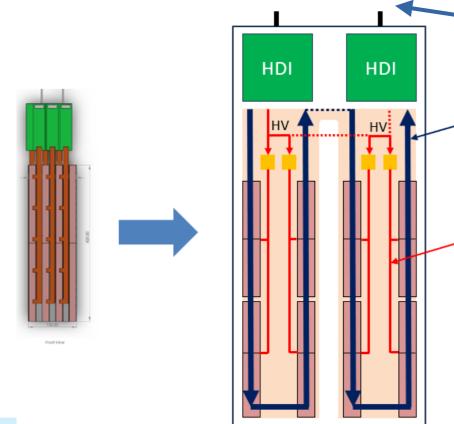






### Power

#### **Serial Powering**



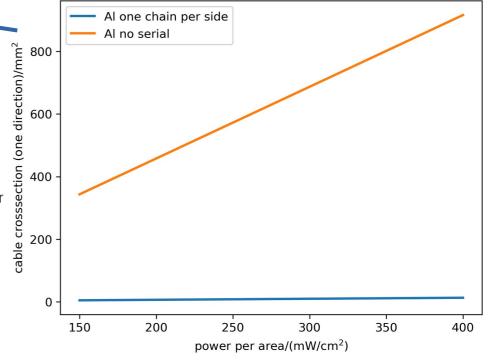
#### Here we don't want to have the thick cables!

#### Serial Powering, 2 options:

- 1. Across 2 columns, 8 drops
- 2. Across 4 columns, 16 drops

Sensor Bias (HV), HV switch per columr

- 1. Single HV feed to module
- Dual feed for redundancy





Integrated MT module