

Belle II Germany Meeting 2025

Tuesday 9 September 2025

Hardware and Upgrade - 3.020 (09:00 - 10:30)

-Conveners: Christian Bepin; Isabel Haide

time	[id] title	presenter
09:00	[26] PXD Status and Operation	SCHMITZ, Jannes
09:25	[56] Upgrade of the Belle II Vertex Detector with depleted monolithic CMOS active pixel sensors	BUCH, Yannik
09:50	[57] GNN-ETM - Design and Deployment of a Graph Neural Network Trigger for the ECL	HAIDE, Isabel

Hardware and Upgrade - 3.020 (11:00 - 12:30)

-Conveners: Christian Bepin; Isabel Haide

time	[id] title	presenter
11:00	[23] First Studies of a GNN based ECL Trigger on Versal-FPGAs for the Belle II Upgrade	LOBMAIER, Thomas Gideon
11:25	[24] Upgrade of the Belle II First-Level Neural Track Trigger by Three-Dimensional Hough Finding and Deep Neural Networks on FPGAs	HIESL, Simon
11:50	[25] Design of the OBELIX Monolithic Active Pixel Sensor for the Belle II VTX Upgrade	WALSEMANN, Alexander

Hardware and Upgrade - 3.020 (14:00 - 16:00)

-Conveners: Christian Bepin; Isabel Haide

time	[id] title	presenter
14:00	[58] All-silicon detector modules for the inner pixel layers of the VTX upgrade	ULM, Andreas
14:25	[60] PXD Emergency Shutdown Investigation	SCHOLZ, Paula
14:50	[61] DePFET X-ray Irradiation Studies	GIAKOUSTIDIS, Georgios
15:15	[59] Challenges Of Hardware-Software Co-Design Of A Graph Neural Network For The ECL Trigger	BAPTIST, Frank Michael