

Session Program

Oct 22 - 24, 2025

Belle II Trigger/DAQ Workshop 2025

Upgrade

大阪公立大学文化交流センター (OMU Academic Extension Center), 大セミナー室 (Large seminar room)
大阪市北区梅田1-2-2-600大阪駅前第2ビル6階 (Osaka Station Building 2, 6th floor, 1-2-2-600 Umeda, Kita-ku, Osaka)

Thu, October 23

2:45 PM

Upgrade

Session |

Location: 大阪公立大学文化交流センター (OMU Academic Extension Center), 大セミナー室 (Large seminar room), 大阪市北区梅田1-2-24600大阪駅前第2ビル6階 (Osaka Station Building 2, 6th floor, 1-2-2-600 Umeda, Kita-ku, Osaka) |

2:45 - 3:10 PM

VTX

Speaker

Markus Friedl

3:10 - 3:35 PM

ITT

Speaker

Jennifer Ott

3:35 PM

3:55 PM

Upgrade

Session |

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3:55 - 4:20 PM

CDCFE upgrade

Speaker

Yu Nakazawa

4:20 - 4:40 PM

CDCTRG dataflow upgrade

Speaker

Hanwook Bae

4:40 - 5:00 PM

CDCTRG logic upgrade

Speaker

Torben Ferber

5:00 - 5:25 PM

ECL shaper DSP upgrade

Speaker

Raffaele Giordano

5:25 - 5:45 PM

ECLTRG dataflow upgrade

Speaker

Yuuji Unno

5:45 - 6:05 PM

ECLTRG logic upgrade

Speaker

Isabel Haide

6:25 PM

6:05 - 6:25 PM

Discussion

Fri, October 24

10:00 AM

Upgrade

Session |

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10:00 - 10:20 AM

Review of trigger menu and physics

Speakers

Prof. Christopher Hearty, Torben Ferber

10:20 - 10:40 AM

UT5

Speaker

Taichiro Koga

10:40 - 11:00 AM

CEF, AI

Speaker

Yun-Tsung Lai

11:00 - 11:20 AM

FTSW4

Speaker

Mikihiko Nakao

11:20 - 11:40 AM

Level3 trigger

Speaker

Dr Seokhee Park

11:40 AM - 12:00 PM

Discussion

12:00 PM

1:20 PM

Upgrade

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1:20 - 1:40 PM

PCIe400

Speaker

Satoru Yamada

1:40 - 2:00 PM

Hardware HLT: FPGA

Speaker

Qidong Zhou

2:00 - 2:20 PM

Hardware HLT: GPU

Speaker

Ryosuke Itoh

2:20 - 2:40 PM

Future event-builder

Speaker

Soh Y. Suzuki

2:40 - 3:00 PM

Anomaly detection by machine learning

Speaker

Takuto Kunigo

3:00 - 3:30 PM

Discussion

3:30 PM