

Introduction to the 1st OBELIX dedicated meeting



■ Current VXD-upgrade status

- EOI have been submitted <https://confluence.desy.de/display/BI/Upgrade+EOI+Submission>
- Highlights of UAC feed-back: (see <https://indico.belle2.org/event/3598/>)
 - “...EOI collaborations should continue their present R&D efforts”
 - “...expected to provide by mid-2022: results on tests of “proof-of-principle” prototypes...R&D on integration concepts...”
 - “...in some cases the estimate and plans are too optimistic, being driven by the need to fit the preliminary deadline of 2026 for installation of the upgraded detector.”
- Of course no decision yet on: upgrade, technology for VXD, budget
⇒ still clear incentive to move forward with the R&D

■ Our goal today

- Start the discussion among chip designers ⇒ start the design work
- Provide initial guidelines for integration ⇒ module design can move forward

Final disclaimer: This is quite the informal meeting and we will need recurrence