

- **Tomasz Hemperek (~50%)** – integration, digital design, verification
- **Hans Krüger (~20%)** – I/O, power, system
- **Marco Vogt (~20%)** – silicon module, DAQ
- **PhD Student (100%)** – digital design, verification, DAQ
- **1 new postdoc (NN, 100%) ?**
- **Belle II PIs:** Jochen Dingfelder, Florian Bernlochner

