

PXD Status

—

DAQ, ONSSEN (ROI and HLT Feedback),
SlowControl (RC, HVC) & Monitoring

- Setup
- EB Interface
 - Load Balancing
- HLT Interface
 - Event selection
- ROI selection
- (Data-)rates
- ~~Gated Mode~~
- Slow Control
 - RC
 - HVC – Issues
- DQM

Reminder: PXD System Layout

Total of 0.2% of X

2 layers: @1.4(2.2) cm

Pixels: 50 x 60(75) μm

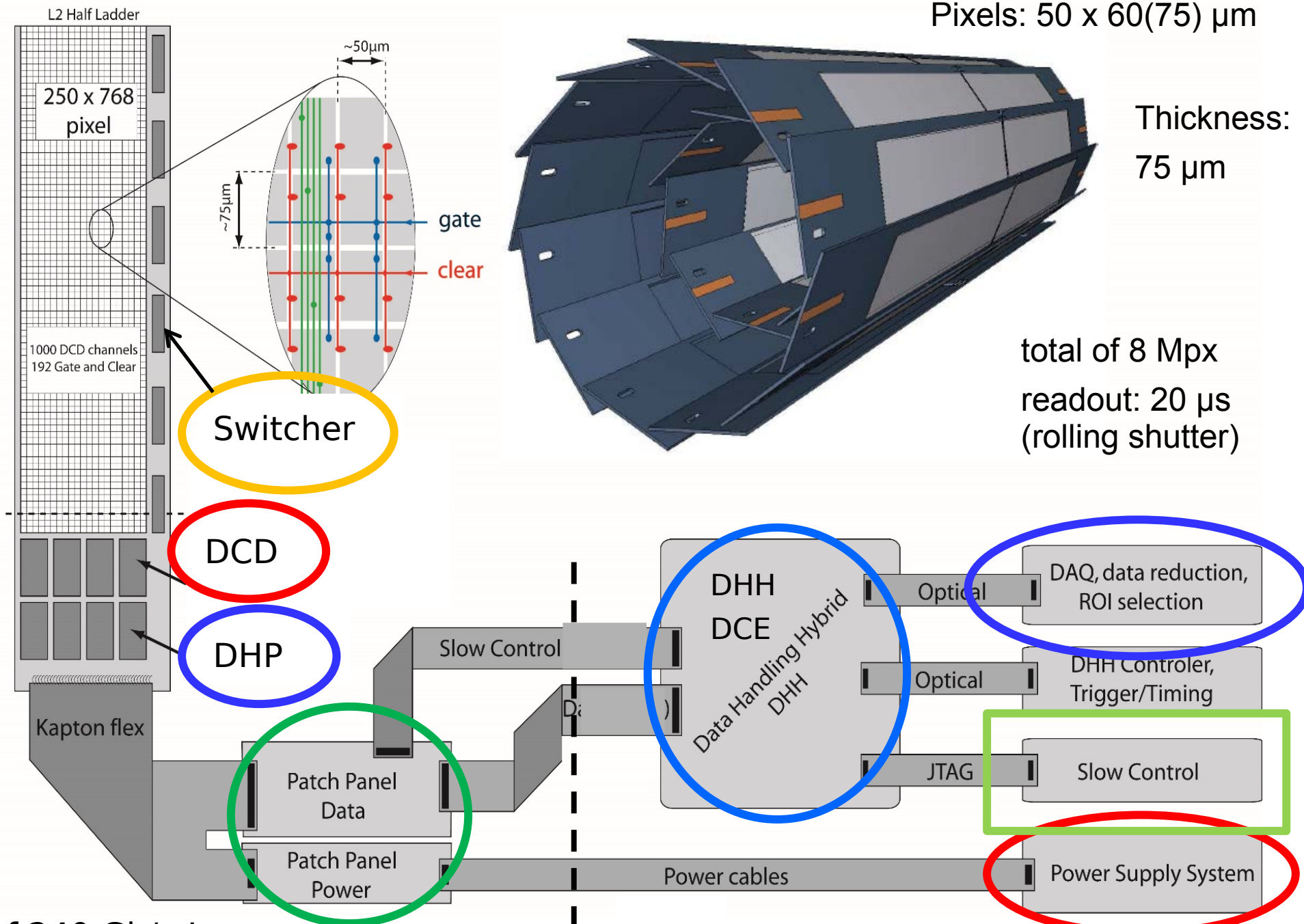
Thickness:
75 μm

total of 8 Mpx
readout: 20 μs
(rolling shutter)

half
ladder:
768
rows

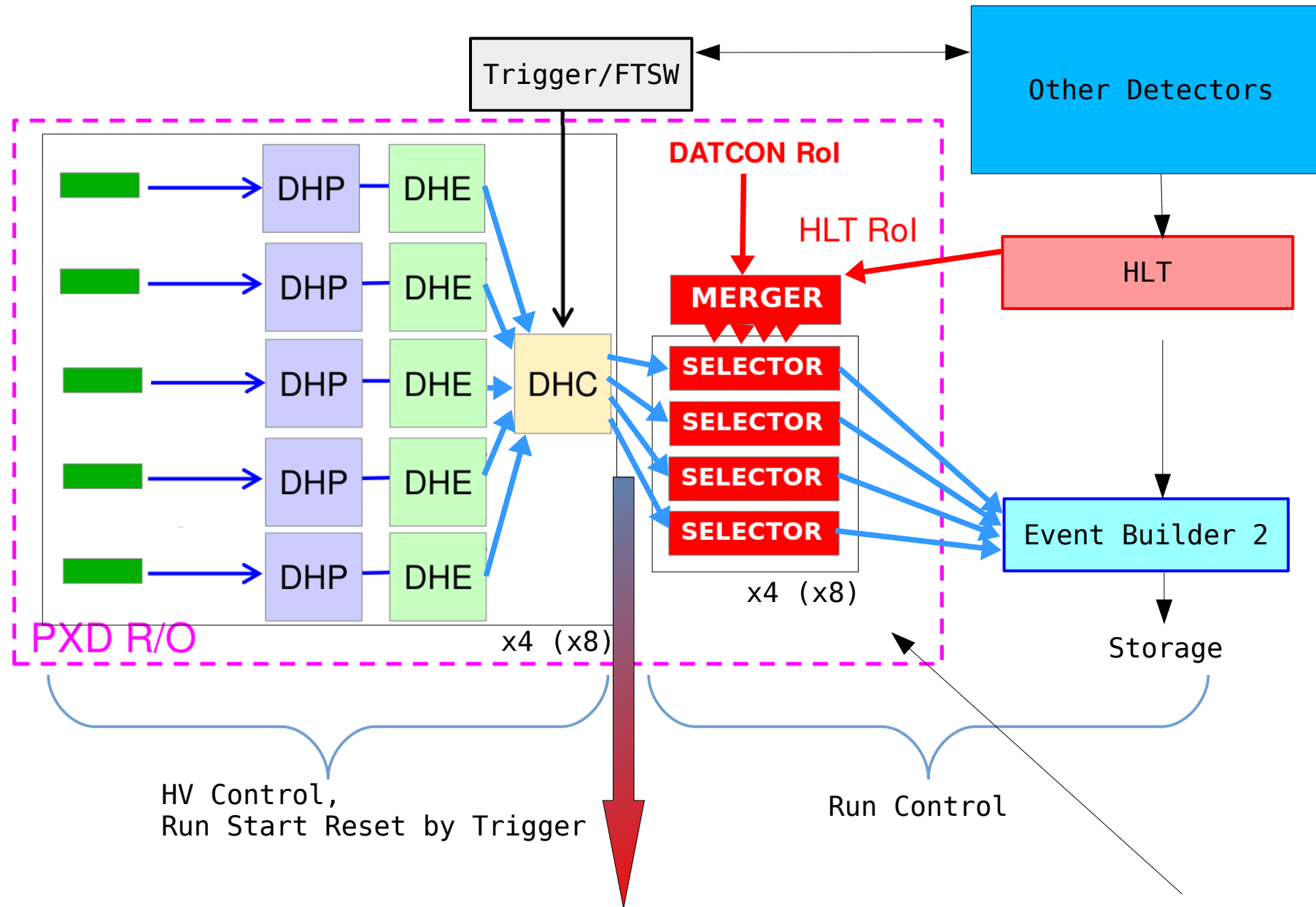
250
cols

15 x
70
(85)
mm



total of 240 Gb/s !

PXD DAQ Scheme Phase 3 minimal (full) Setup



Data path to local "Bonn" DAQ,
but only sub-set of events.
Pedestal and calibration!

Without load balancing we had only one
active output per DHH, thus effectively
using only 4 selector boards altogether

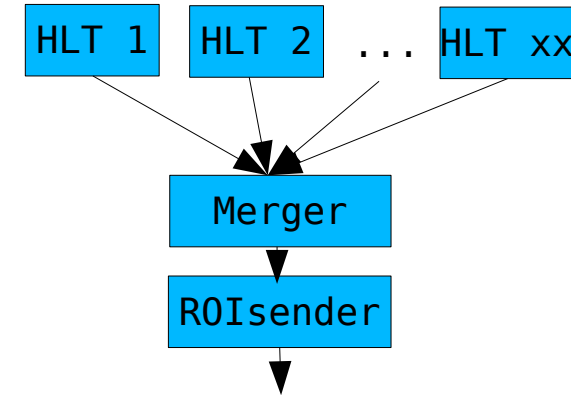
- Only $\frac{1}{2}$ of PXD (20/40) modules installed
 - Two broken, damages on others
 - Only half of readout used (only $\frac{1}{2}$ DHH and $\frac{1}{2}$ of ONSSEN powered off)
- Mostly stable data taking
 - 5kHz real data, 10kHz DAQ test, 35kHz with dedicated tests
 - Event selection on HLT (and ONSSEN!) turned on now (event reduction to $\sim 10\%$)
 - ROI selection tested in few runs (for half the modules \rightarrow DHE order)
- Overlapping trigger firmware deployed during phase 3
- Only Belle 2 trigger veto for continuous injections, no gated mode (yet)

- Long HLT startup time, long buffering and processing times for some events (up to 2 minutes!), challenge for ONSEN buffering → full → trigger busy at each run start
- HLT does not clear buffers and/or worker nodes crash → for some events we never get triggers → still in memory at run stop
- NSM timeout EB/HLT → PXD seen as excluded (seems to be solved)
- “HLT before DHH” ← no data sent anymore from DHH (in most cases)
 - Firmware and/or ASIC issue
- Not all problem come from PXD even if PXD notices an error
- DHH firmware was updated/changed several times
 - PXD Busy and data corrupting happen with some versions
 - Different reasons → fixed with latest versions
- Lost 25% of data because of wrong trigger delay setting in early phase 3
- One optical link (of 80) went dead in June

- Stable
- Few unexplained crashes on EB side
- Changes for $\frac{1}{2}$ PXD and Load Balancing, different distributions schemes
 - Number of links from PXD to EB
 - Order in which events are distributed on the links
 - \rightarrow configurable, tested and implemented from 2018 on (Simon, Yamagata, BS)
 - Verified again this summer
- NSM timeout issue (“PXD not included”)
- If some events are not coming from HLT \rightarrow ROISENDER \rightarrow ONSSEN, we will run out of sync at EB immediately.

- Needed for full bandwidth!
 - Scheme on DHH, ONSSEN and Event Builder need to be configured identical
- Changes
 - 1 → 4 outputs per DHC
 - 4 → 16 ONSSEN selectors used
 - 4 → 16 EB inputs used
 - Each 4th event goes to one EB input
- (still ½ of the full system)
- ONSSEN and EB scheme prepared and tested already in 2018
 - Worked out of the box, no surprises
- Recently tested without modules and w/o ROI selection

- “ROISENDER” and “MERGER”
- Stable
 - Lot of bug fixes and improvements before phase 3
 - Changes ahead: get away from mqueue
- NSM timeout issue (“PXD not included”)
- If some events are not coming from HLT → ROISENDER → ONSEN, we will run out of sync at EB immediately.
- HLT:
 - Events still got lost (not only dying workers)
 - Events still take too much time (>2min!)
 - Main impact: high trigger rate tests
 - Stale events from last run → now: better buffer flush before HLT STOP
 - (more a problem for EB, not so much for ONSEN)

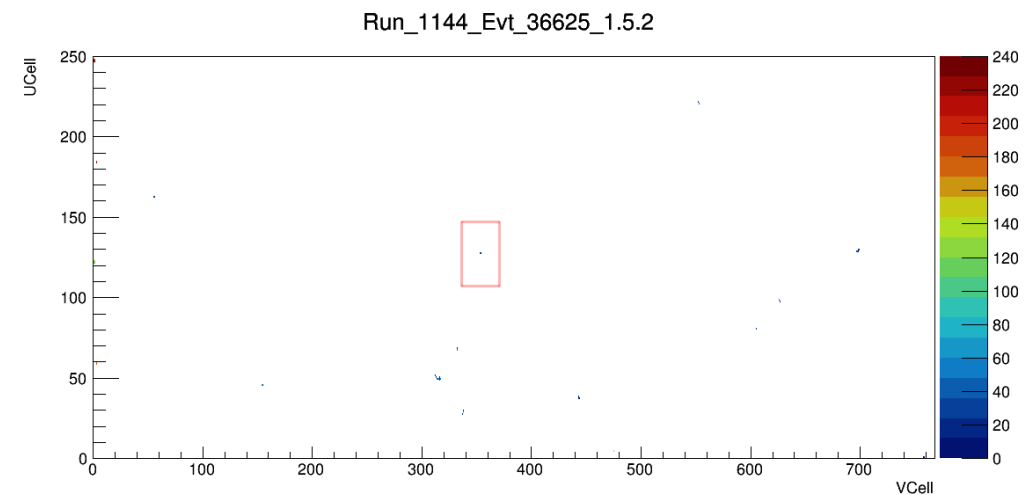
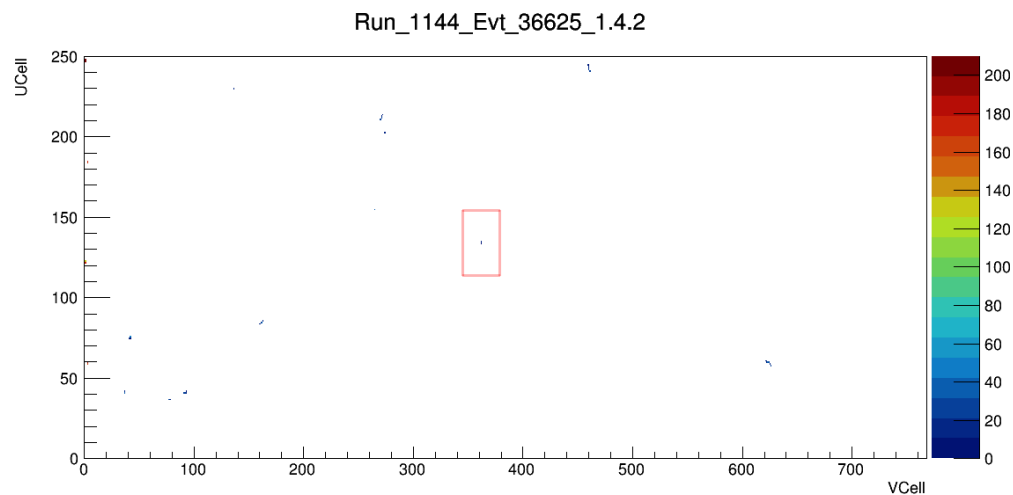
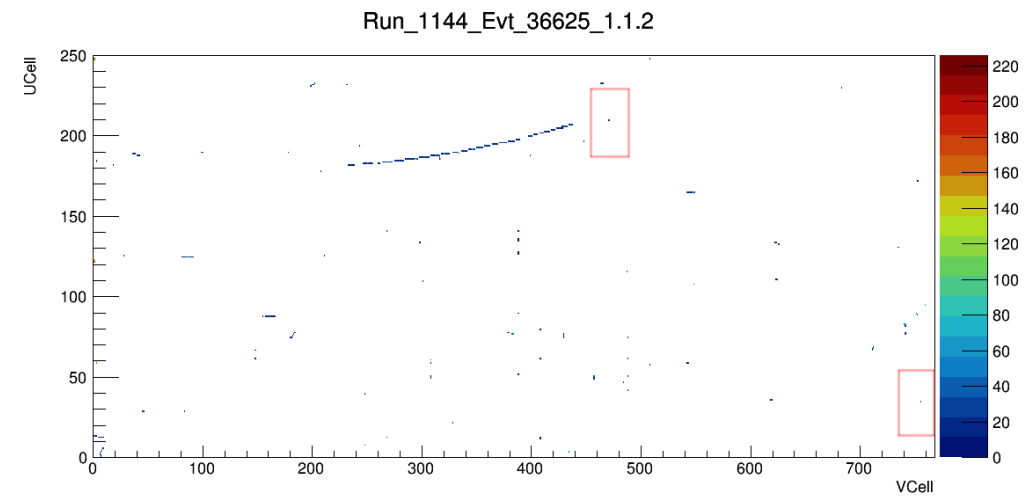
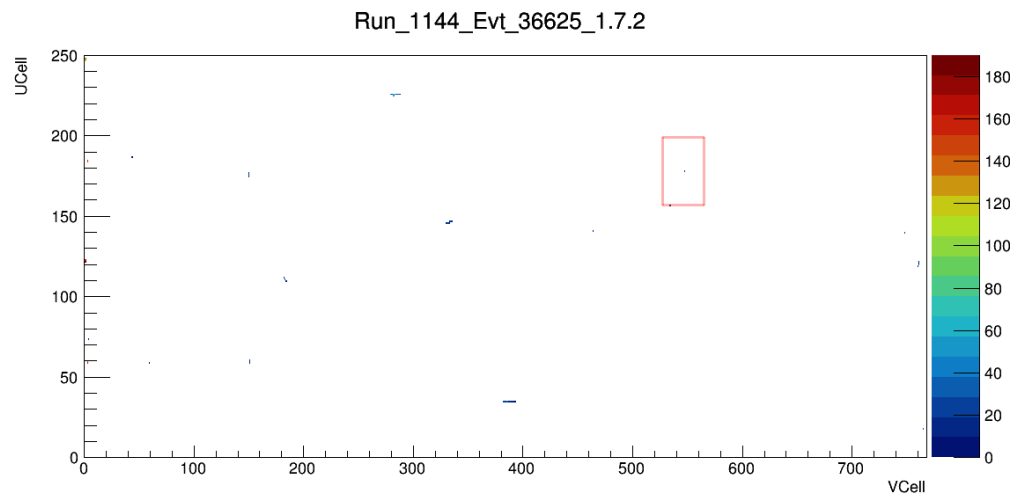


- Real High Level Trigger decision
- Decision is encoded into packet for ONSEN
 - ONSEN rejects data payload and send only header
- No problem found on PXD side
- Remark: we still have to send empty packets, they account for small additional fraction of bandwidth

- ROIs are calculated on HLT and send to ONSSEN
 - **Always enabled.** Even if ROI selection is disabled (in ONSSEN slow control)
 - (DATCON as additional source, was not used until now.)
- ONSSEN filters PXD hits based on these ROIs

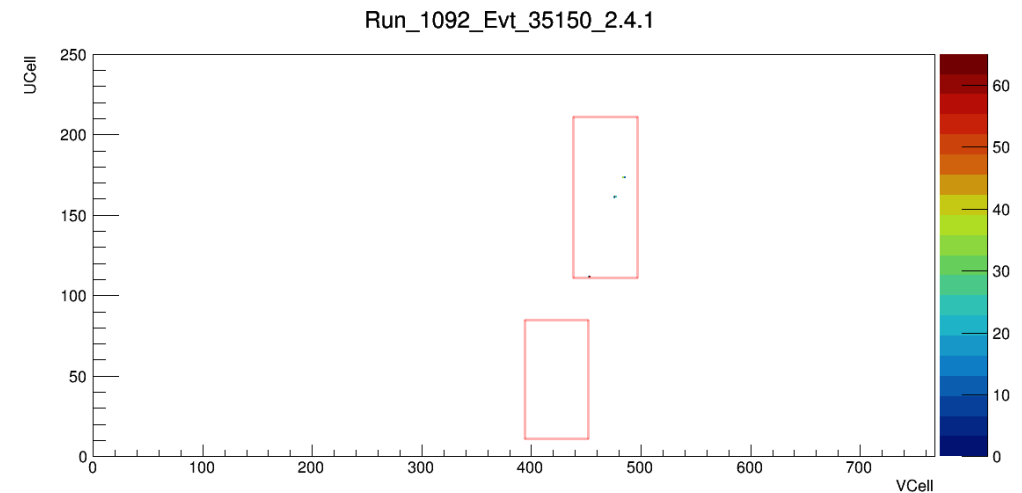
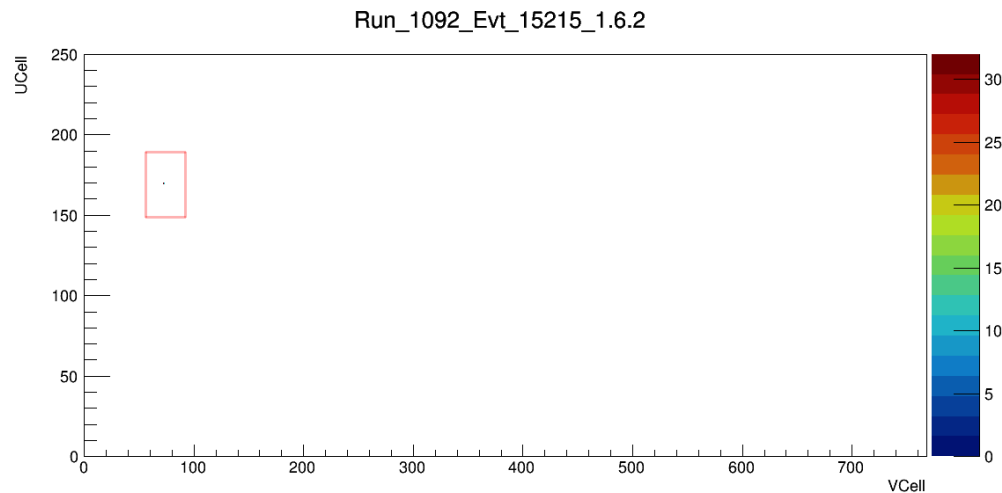
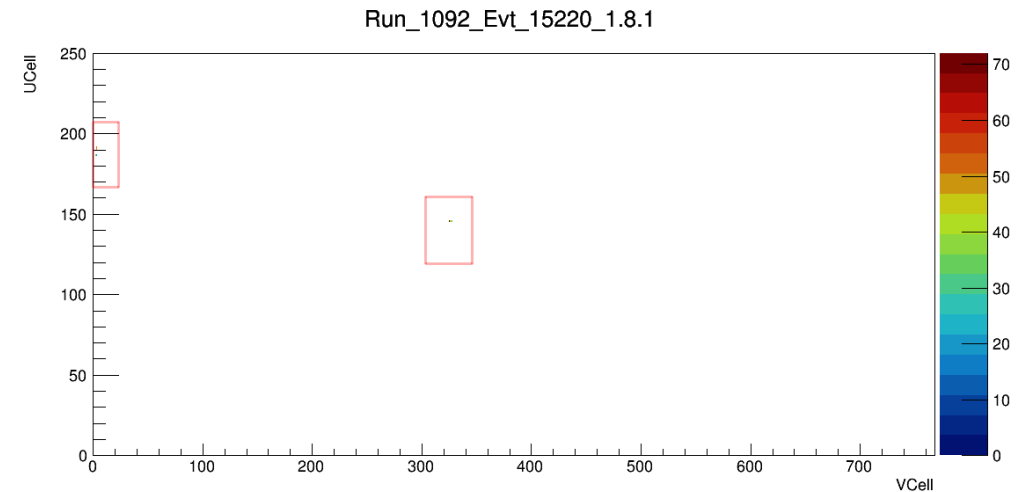
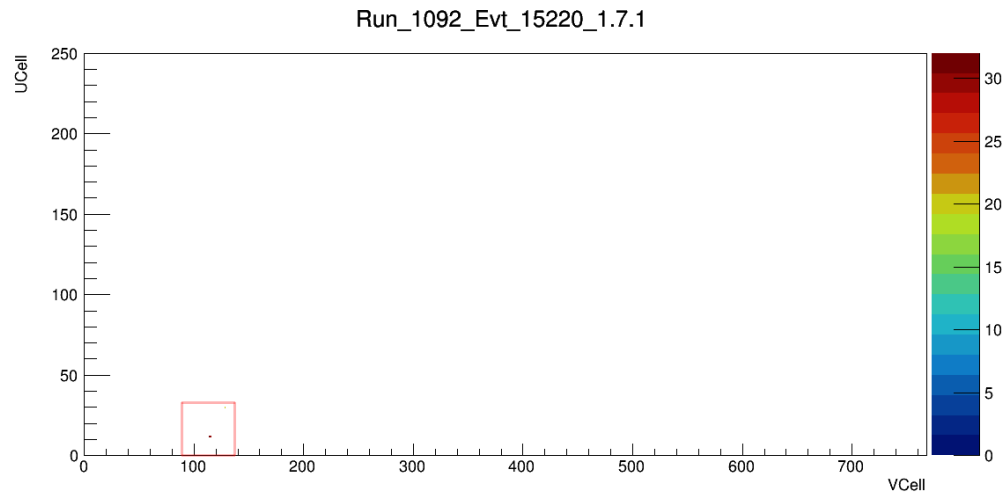
ROI Selection OFF, HLT ROIs Overlayed

Randomly chosen good event



Cluster seem to be centered in ROI → extrapolation on HLT seem to have good quality

Randomly chosen good events



Cluster seem to be centered in ROI → extrapolation on HLT seem to have good quality
But: DHE Order problem, half of the modules have not data after selection
Selection worked as expected → verified in pixel-by-pixel check against BonnDAQ data

- ROI processing was turned on as a test for several runs. As expected, data for modules which were out-of-order is empty (→ no ROIs)
 - Offline check against BonnDAQ data for other modules:
 - → everything is selected as it should
- ROI selection needs ascending module ID in data and trigger for matching
 - Not fulfilled in beginning of phase 3
 - Cables within connectors in dock box and on DHH RTM could not be swapped.
 - ROI selection could only be verified for half of the modules (→ B2GM talk)
- Reordering of inputs from PXD Module → DHE → DHC
 - Optical switch has been installed in summer shutdown
 - Full ROI scheme still need testing
- Quality of ROIs (=Tracking Quality & Size) t.b.d.
 - Efficiency for low pT

- High occupancies together with triggers following shortly after each other → “link dropped” (actually the link is still there, but no data is coming for the current event)
 - DHP fifo full (CM=63)
 - Mainly during HER injection, ‘dirty bunch’
- This won’t stop the DAQ → manual SALS
- DHP ASIC limit
- Workarounds
 - 1: Increase the trigger veto length thus we should not get triggers when we have highest occupancy
 - 2: Gated Mode (todo)
 - 3: Reset DHP during injection veto time thus the dropped links would be limited to a time span between two injections.

- Without DHH → 40kHz and more have been tested (already last year)
- Two explicit tests June 21st and June 25-26th
- Occupancy (0.052%=112 pixels); not saturate the link to EB (4*100MB/s max)
- Without event selection and ROI selection, the data rate at the output to EB is factor 30-50 higher than realistic case.
- run in global: null, poisson, HLT: passthrough & beam_reco
- 2us holdoff, 10kHz, 20kHz, 30kHz, 50kHz input trigger rate

- Main problem: Missing (trigger) events from HLT, memory is filling up by “forgotten” events
- Summary (after some fix was applied on HLT)
 - 30 kHz worked fine for DHH and ONSSEN. HLT script passthrough (no processing at all)
 - 50 kHz is limited by DHH (most likely) to 35 kHz at 0.052% occupancy

This run was performed with `beam_reco_filter`. Since HLT threads die and sometimes take > 200 s for processing, PXD data in ONSSEN memory get stuck. I think, 50 kHz and passthrough was not performed. But since we observe different causes of limiting factor, it's clear that this works as well up to 35 kHz. Hopefully HLT processes will not die such frequently after switch HLT handling to ZMQ. While running with PXD only, HLT load was quite low and not limiting factor of performance.

- Remember: the selected the occupancy depending on what you want to test → if we are dominated by back pressure from EB, we cannot go to higher rates. Realistic test would need load balancing and data reduction by 1/5 event and 1/10 ROI selection

- No big problems since January, no big changes
- Restart procedure, environment for EPICS have been improved
- RC Status of HLT, EB connection to PXD improved, but I think it is not always showing error if the connection was dropped (crash?)
- NSM timeout EB/HLT → PXD seen as excluded (seems to be solved)

- Working as designed, but maybe not what is wanted
 - Injection inhibited while HV is in error (after over voltage protection shutdown) turning on/off or interlocked (cooling)
 - Removed TURN ON/OFF from state machine response (this is not what is intended...)
 - Automatic transition from ERROR to OFF
- Ongoing **discussion** ...
- Observed several OVP in phase 3. → PXD shifter need to intervene as error state is blocking injections. Not clear yet how we can fit some (partly) automatic recovery in the current HVC scheme.

- PXD SC: no major issues, hardware was upgraded
 - IOCs, GW, archiver, monitoring...
- Alarms system & alarm tree
 - DHP temp measurement, occ. Drops, links ...
 - New: Interface to RocketChat
 - (independent development from the one which is now used for DAQ/Zabbix)



#pxd_log

PXD Log and Alarm Messages

Log message — **DHE62** : chip 3 does not support IDCODE.

PXD logging @michael.ritzert 16:21

Log message — **/psApp/** : Unit 79: emergency shutdown: OVP

PXD alarm system @michael.ritzert 16:21

Alarm triggered – PV: **PXD:P1081:status-ovp:S:cur**, State: **MAJOR** (STATE_ALARM) [here](#)

PXD logging @michael.ritzert 16:21

Log message — **/psApp/** : Unit 79: emergency_shutdown

PXD logging @michael.ritzert 16:27

Log message — **DHE62** : No chain with this name in driver

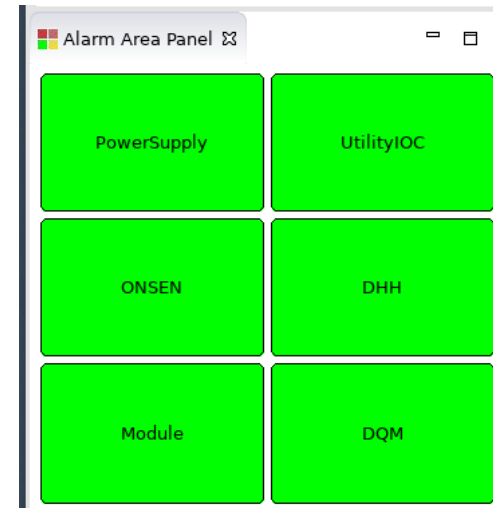
PXD logging @michael.ritzert 16:27

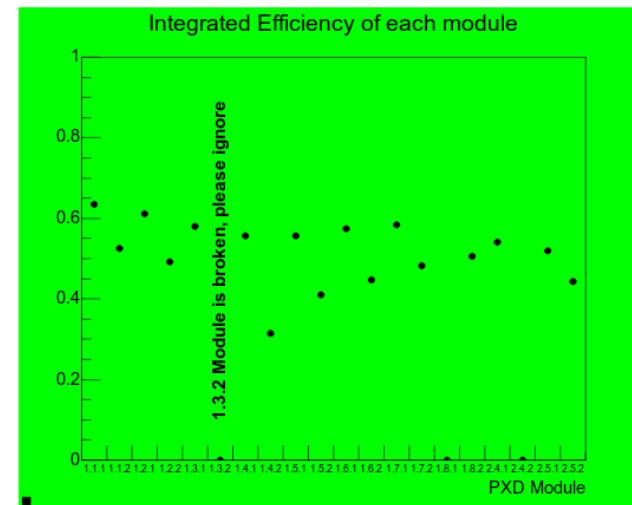
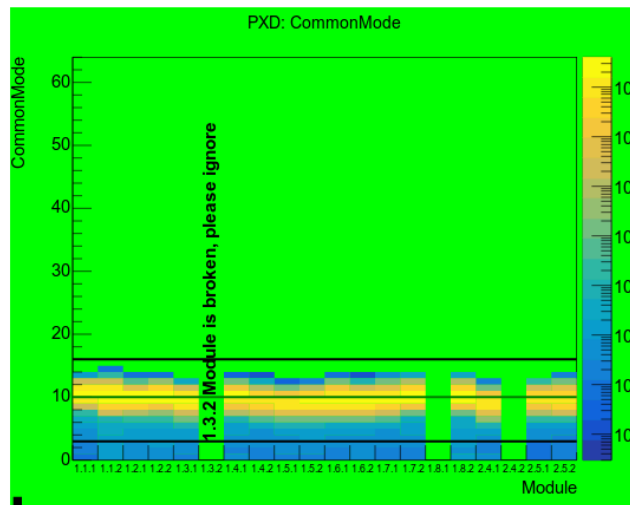
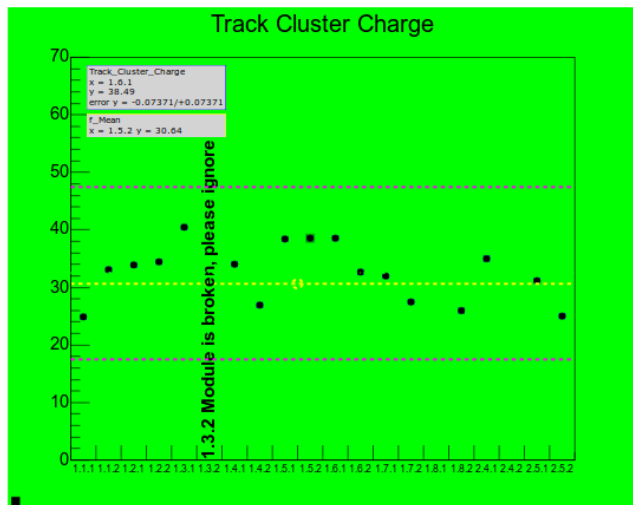
Log message — **DHE62** : Calibration unsuccessful. You may check if ASICs are on.

PXD logging @michael.ritzert 16:27

Log message — **DHE62** : Calibration unsuccessful. Status = -1

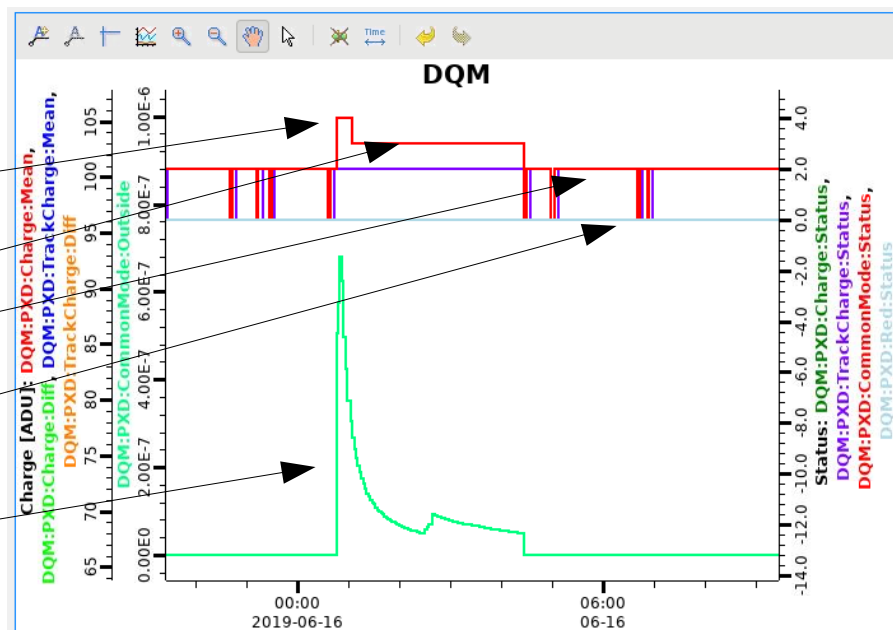
- ▶ ● Area: PowerSupply
- ▶ ● Area: UtilityIOC
- ▶ ● Area: ONSEN
- ▶ ● Area: DHH
- ▼ ● Area: Module
 - ▼ ● System: 1011
 - PV: PXD:1011:occ-dropped:ALRM:cur
 - ▶ ● System: 1012
 - ▶ ● System: 1021
 - ▶ ● System: 1022
 - ▶ ● System: 1031
 - ▶ ● System: 1041
 - ▶ ● System: 1042
 - ▶ ● System: 1051
 - ▶ ● System: 1052
 - ▶ ● System: 1061
 - ▶ ● System: 1062
 - ▶ ● System: 1071
 - ▶ ● System: 1072
 - ▶ ● System: 1081
 - ▶ ● System: 1082
 - ▶ ● System: 2041
 - ▶ ● System: 2042
 - ▶ ● System: 2051
 - ▶ ● System: 2052
- ▼ ● Area: DQM
 - PV: DQM:PXD:TrackCharge:Status
 - PV: DQM:PXD:CommonMode:Status
 - PV: DQM:PXD:Eff:Status





Histogram Status exported to EPICS & archived

RED = CHECK!!!
YELLOW = check
GREEN = OK
GREY = empty
Value the decision
is taken from

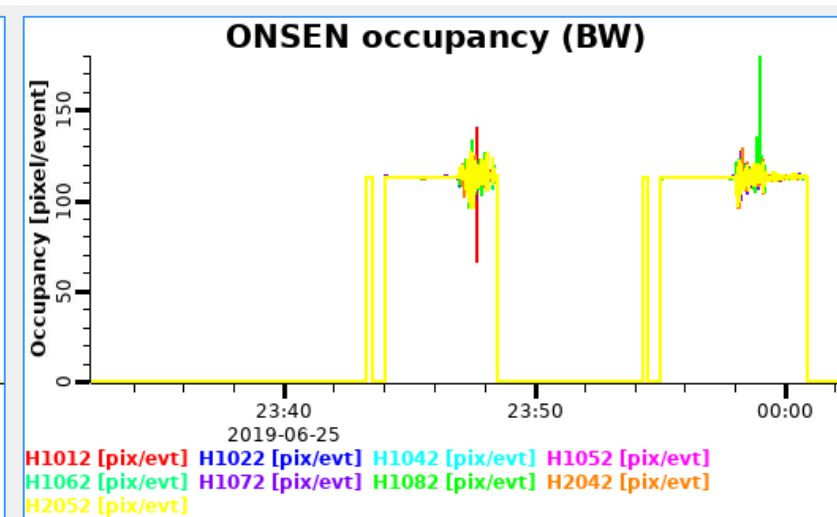
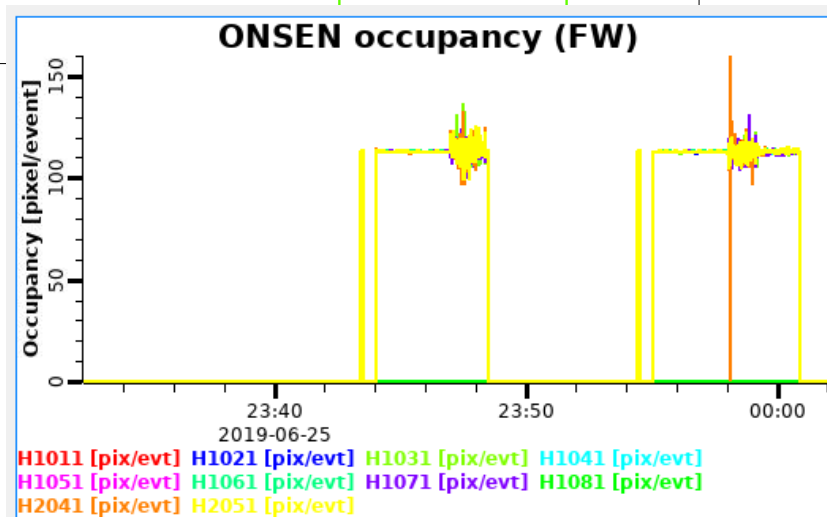
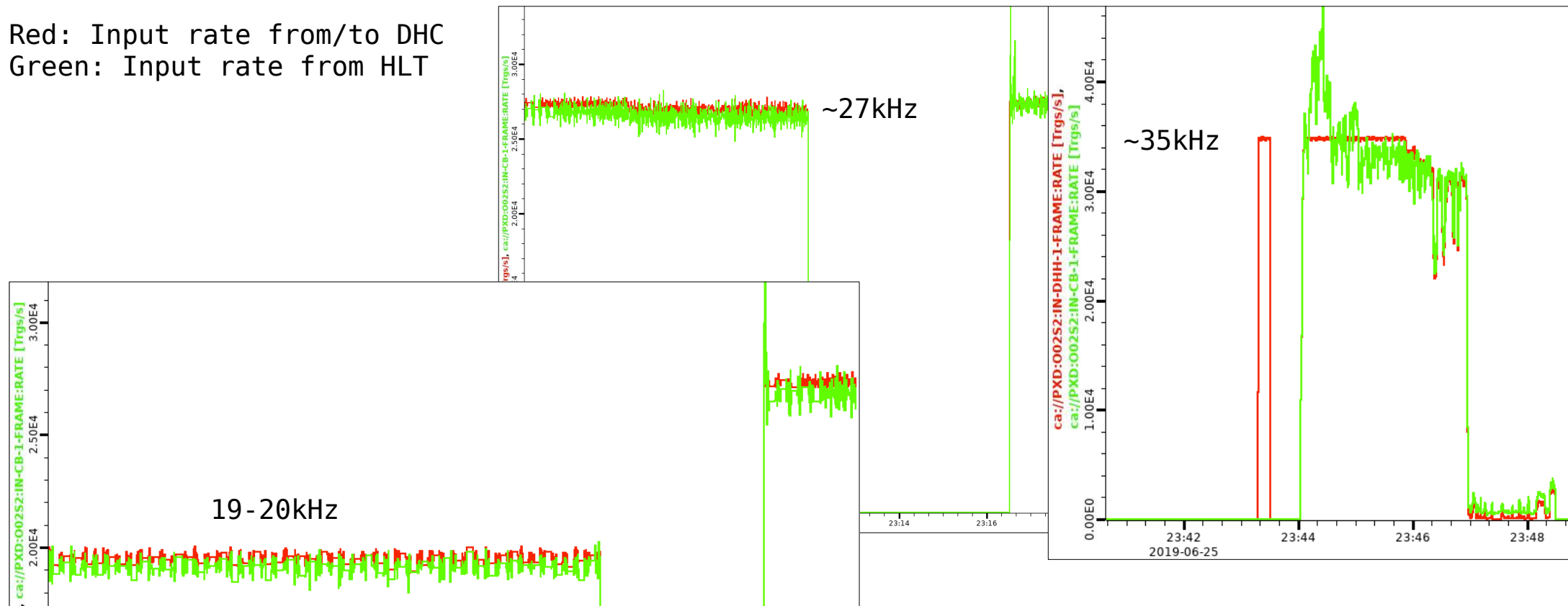


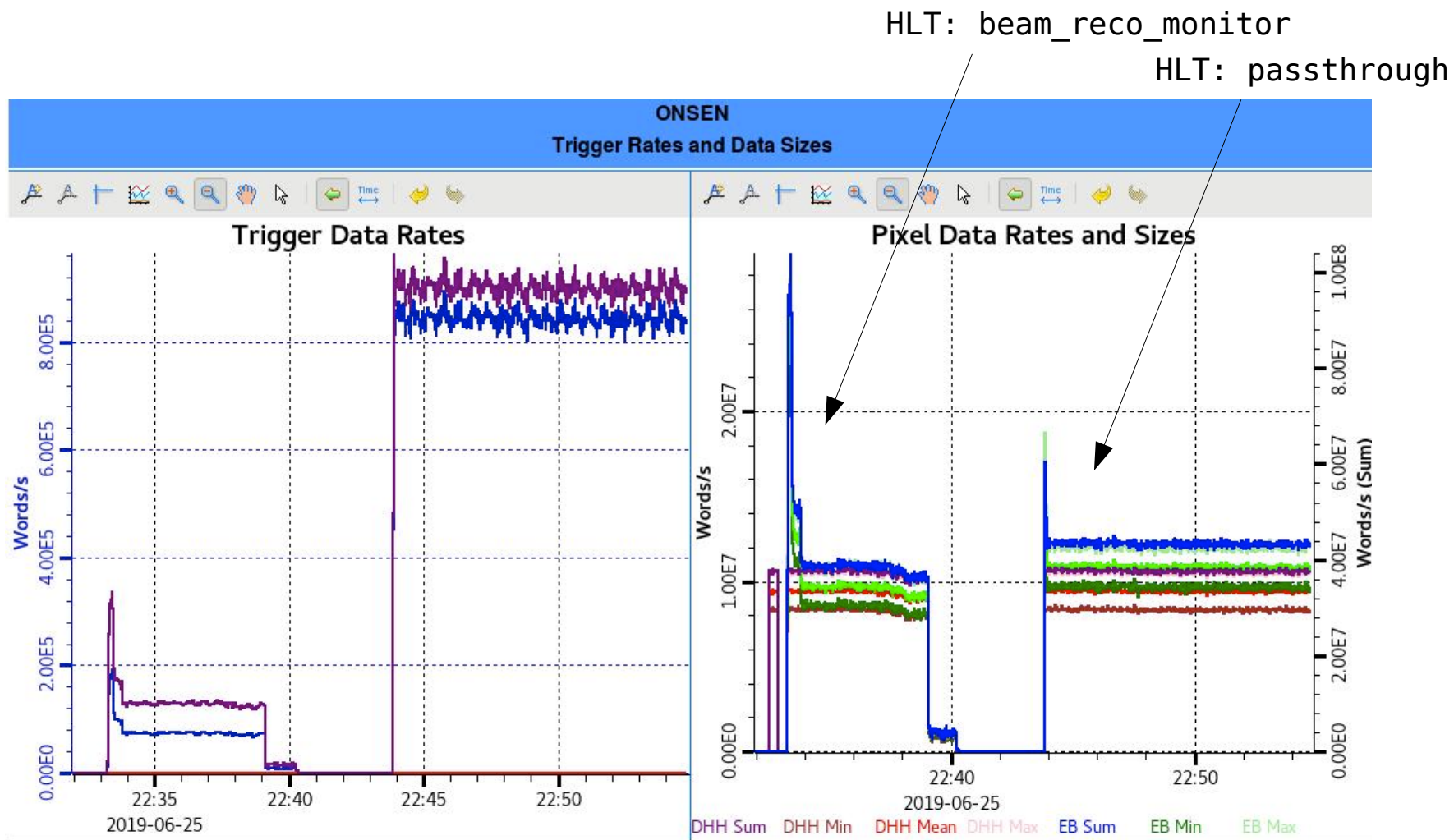
- PXD DAQ basically running stable (towards the end of data taking period)
 - Big troubles at the beginning and before phase 3
- ROI and events selection proved
 - ROI selection for all modules should be working by now
- RC/HVC, SlowControl, monitoring, DQM etc fine
 - Still continuous improvements
- High occupancy burst need some workarounds
- Investigate why we lost one optical link (like in phase 2)
- Improve on the time we need for pedestal taking (esp after some emergency off)
- Not discussed here:
 - Fast emergency off in case of “beam dust” events (change in PSU)

Backups

- Observed some “coincident” errors with other detectors
 - e.g. CDC error just before PXD got busy.
 - Reason still unclear.

Red: Input rate from/to DHC
Green: Input rate from HLT





- Triggered OVP in Nov 2018:
current limits for clear-off to close => increased current limits
- Triggered OVP in April 2019:
frequently: 1072, unit 80, but also other ones less frequent
After update of DHI firmware / JTAG libraries
reason still unclear
=> switched back to old version
- Still under investigation? (DESY long term test)
<https://agira.desy.de/browse/BIIPXDH-289>
- How to handle PS when OVP happens:
bring it into a save state, ramp up (recover)
<https://agira.desy.de/browse/BIIPXDH-259>
- Retuning of gate-on voltages
 - Actual currents as current limit (1 week)

→ See Talk by M. Ritzert

2019-04-26 22:53:55, H1012 ovp
2019-04-24 23:13:11, H1082 ovp
2019-04-26 18:10:33, H1082 ovp
2019-04-29 18:47:30, H1082 ovp
2019-03-29 04:31:56, H1032 ovp
2019-04-08 10:01:30, H1062 ovp
2019-04-24 11:13:48, H1072 ovp
2019-04-24 16:45:01, H1072 ovp
2019-04-27 17:37:56, H1072 ovp
2019-04-28 22:25:31, H1072 ovp
2019-04-29 09:38:33, H1072 ovp
2019-04-29 22:06:42, H1072 ovp
2019-04-30 17:58:45, H1072 ovp

27.05.2019

PXD Workshop and 23rd International Workshop on DEPFET
Detectors and Applications

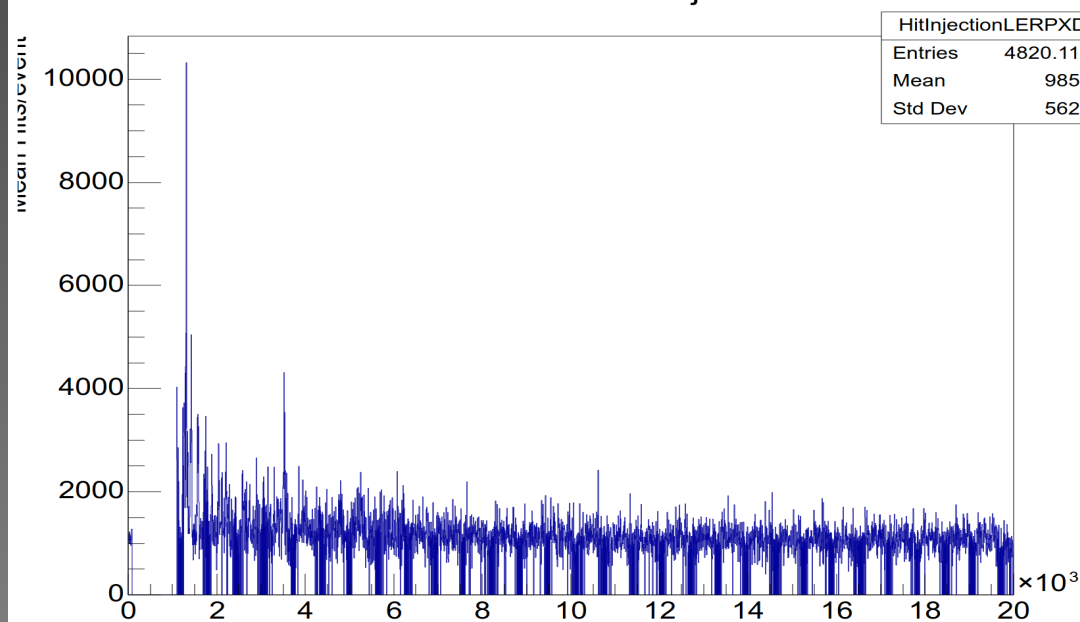
13

- Module emergency shutdown w/o reason due to OVP
 - Not observed for quiet some time (solved?), Reason not completely understood
- Re-evalute logic now because we want a faster emergency off

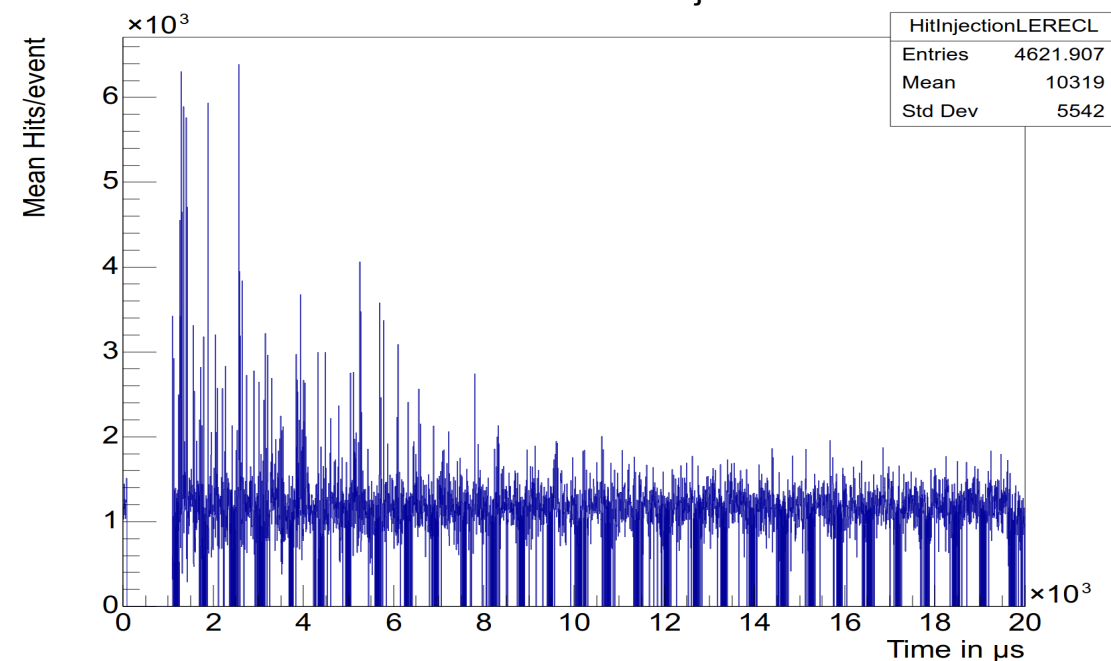
- Trigger delay, DHP latency
 - **We lost 25% efficiency in early phase 3**
 - need physics trigger and knowledge of first gate
- Occupancy calculation (was data rate on DHH → now real pixels on ONSSEN)
- Parallel ramp-up (CPU limited, Log message flood)
- JTAG, pedestal upload & verify
- Workaround for “DHP last gate bug” (was done wrongly for some time)
 - Now fire 2 pixels permanent in second last row in each DHP
- Lot of H1051 (DHI50) data drop fixed by FW update on DHI

- Overlapping triggers FW deployed in early phase 3, basically work
- DHH → DHC+DHE+DHI firmware.
 - Updating led to problems, often unclear which update was responsible
 - Lost time & lumi
 - Test setup at DESY is not a 1:1 copy, agreed to test changes there first
- Current situation is reasonable stable
 - But: Occupancy drops → DHP/DHE interface
- Gated Mode, Signal from FTSW and parameters
 - Test at DESY and KEK → See Talk by F. J. Müller
- Few other features no deployed

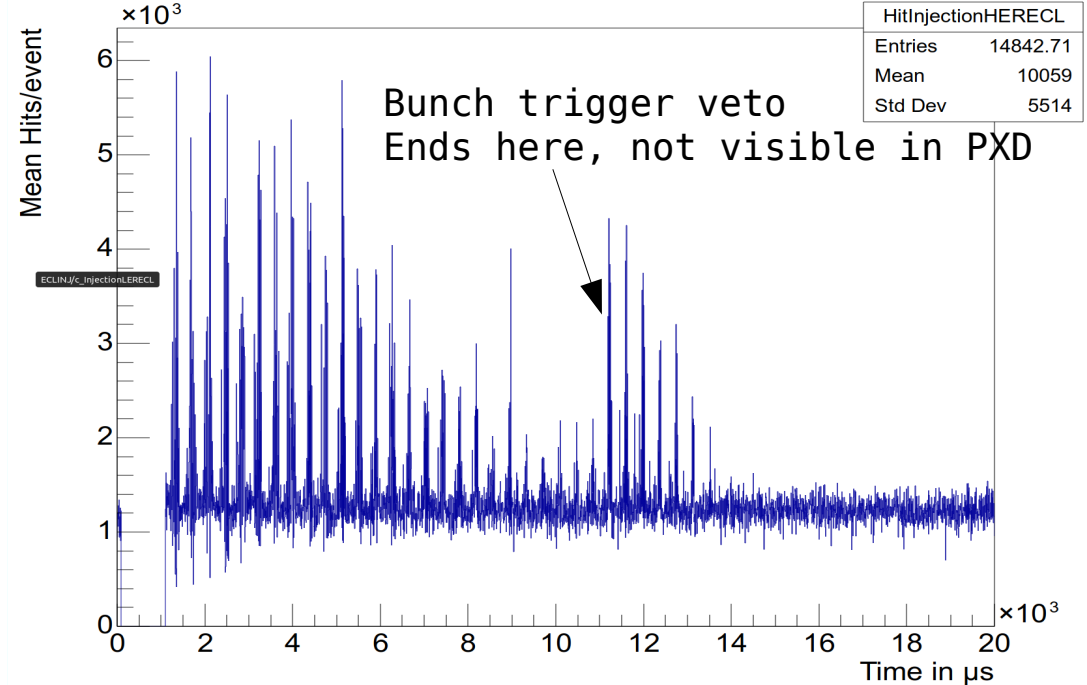
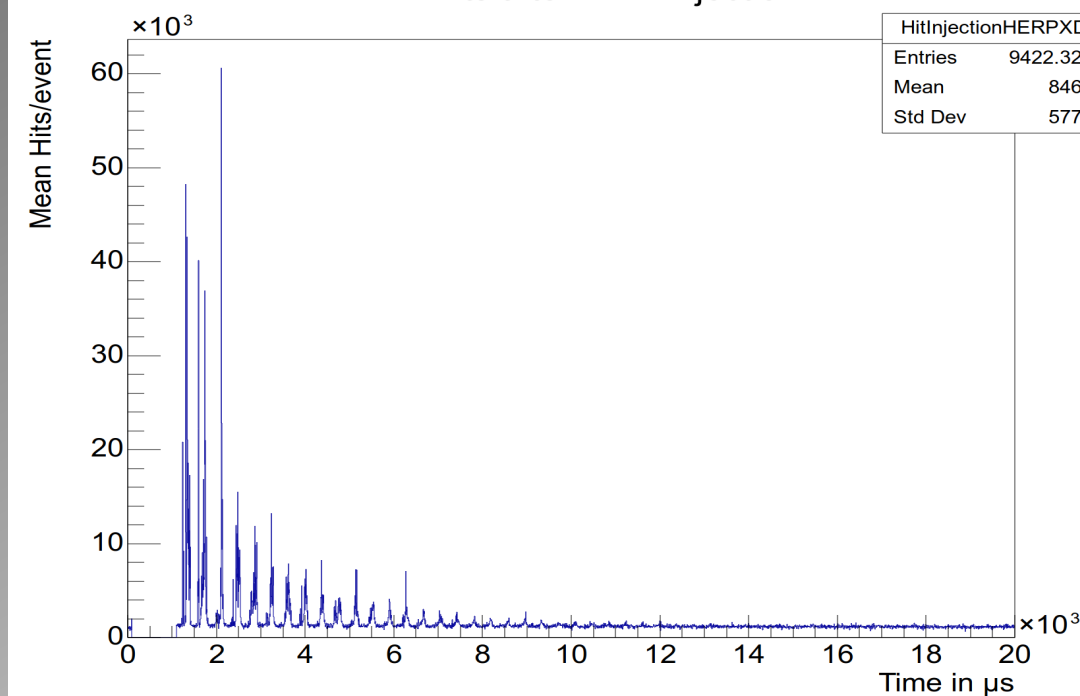
PXD Hits after LER Injection

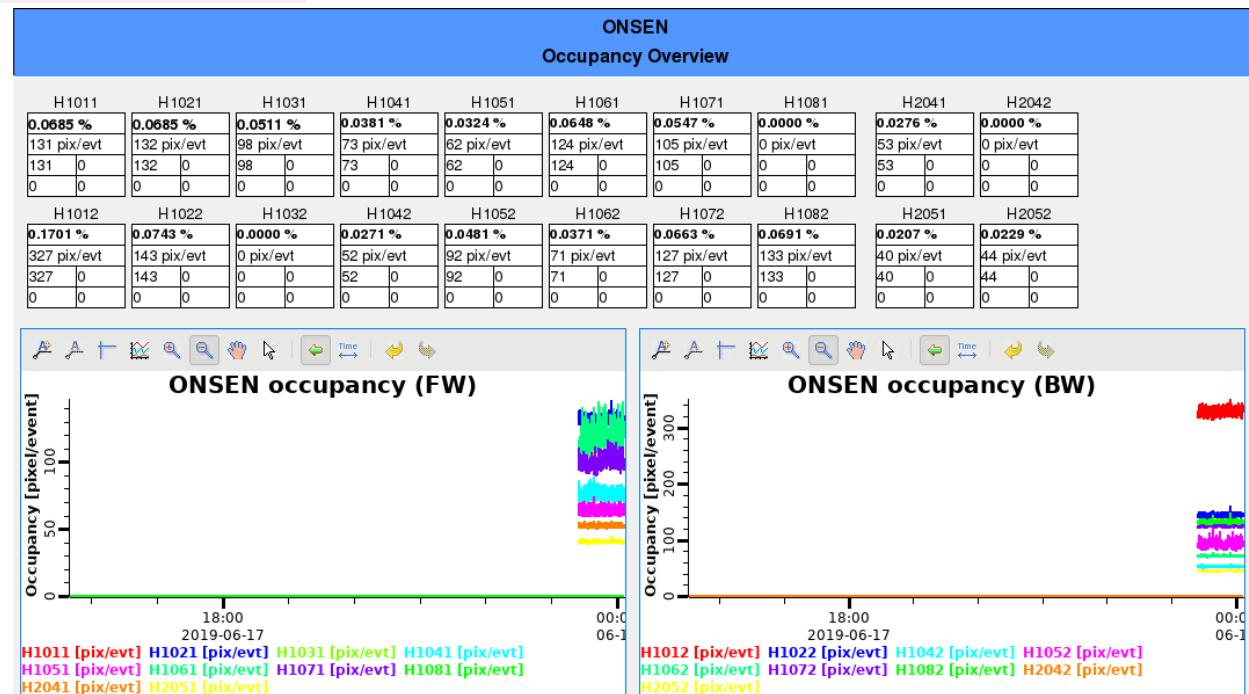
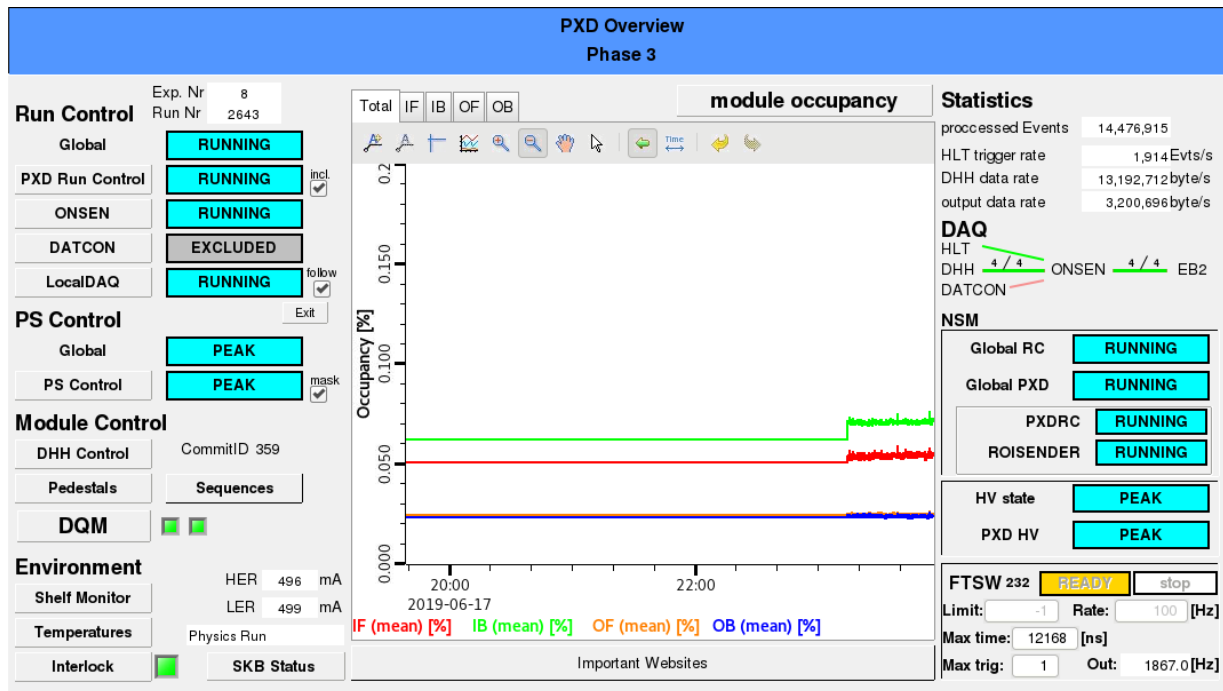


ECL Hits after LER Injection

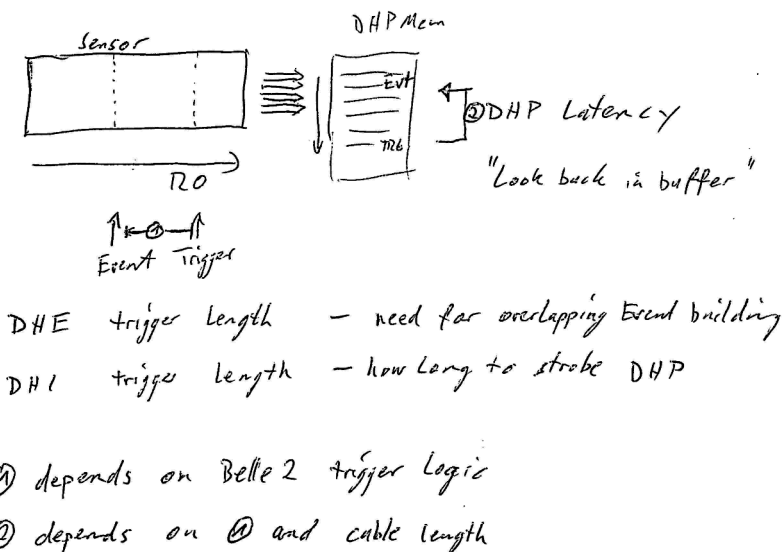


PXD Hits after HER Injection



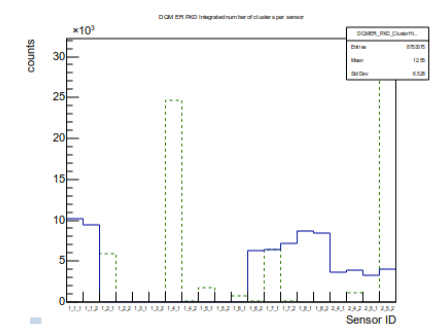
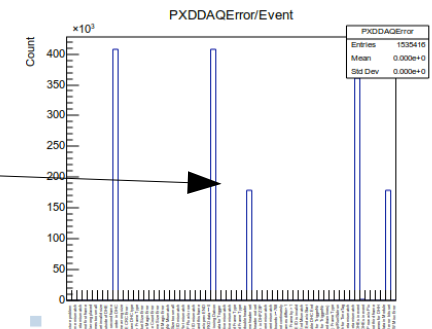
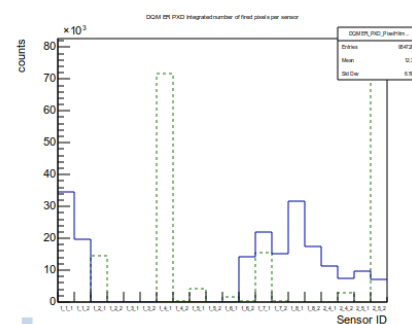
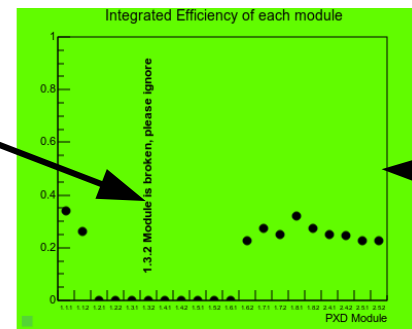
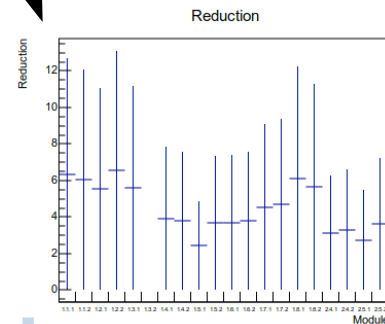
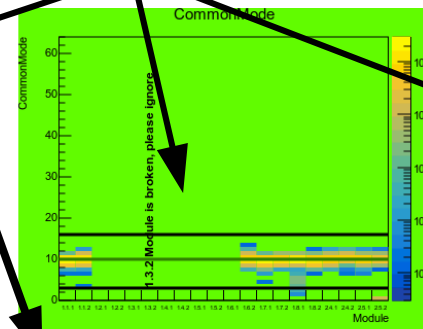
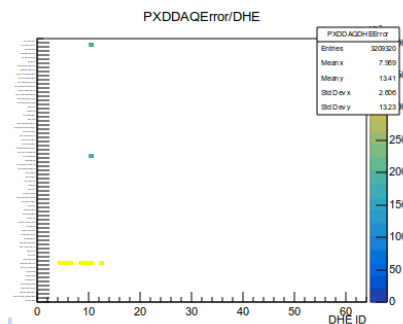
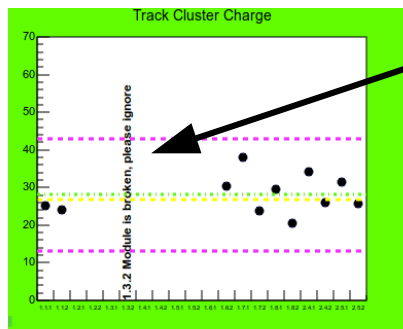


- PXD trigger consists mainly on one parameter:
 - DHP latency (look back in time in DHP memory)
 - 1 gate \triangleq $\sim 100\text{ns}$
- Readout length depends on two parameters
 - DHI trigger length (strobe length to DHP)
 - DHE trigger length (used by the FW to build event)
- Latency depends on the trigger latency and timing of Belle2
 - DHP latency was ~ 50 in Phase2
 - Was set to 5 in early Phase3?
 - 45 gates off \rightarrow $\sim 23\%$ ($=45/192$) **loss of efficiency!**
- Finding correct timing only possible with “real” physic triggers (but low stat with cosmics)
 - Need correct trigger gate (timing pixels) \rightarrow can tune “Start Gate emulator” on DHE, x-check
 - Need to know the readout frame the pixel is firing in (timing pixels), determine when next readout frame starts (per DHP!). Information is lost in overlapping firmware due to event building



- Data reduction at work!
- Effect of wrong DHE order

Event with errors show up as missing Efficiency for all modules (not only the one affected by the error)



- PXD stopped run for two main reasons:
 - DHH did not send data anymore (finally BUSY)
 - DHH send too many corrupted data
 - HLT did not send Triggers* (anymore)
 - HLT did not send Triggers for too many events at the beginning of a run (overwrite)
 - → memory occupancy at end of run
 - Misconfiguration or -operation (after pedestal taking, ioc crash, operator error)
- * this could happened if the HLT did not notice that PXD was included into run (NSM SC timeout!)
 - NSM timeout of EB (have observed that before...)
- EB2 crash → link to ONSSEN drop → PXD ERROR

- Real High Level Trigger decision
- Decision is encoded into packet for ONSEN
 - ONSEN rejects data payload and send only header
- Only in few runs for testing this morning.
- Event selection != ROI selection
 - ROI selection was OFF!
 - Any data reduction is only by event selection
- Seem to work without problem from our side.

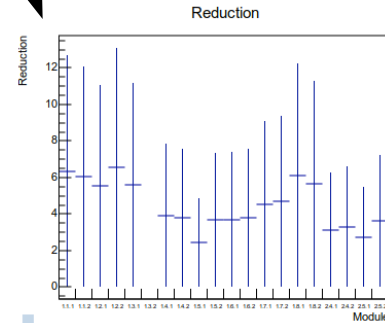
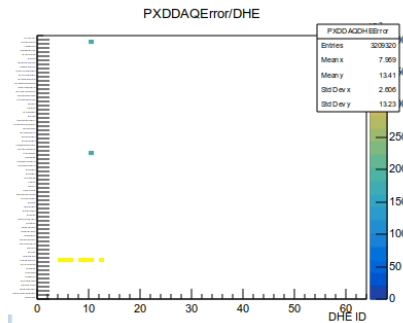
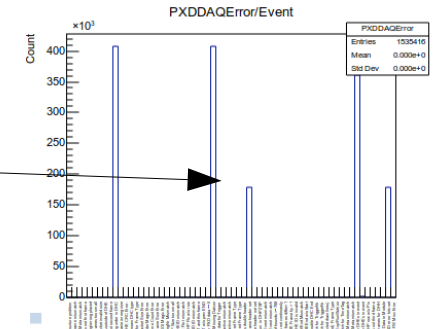
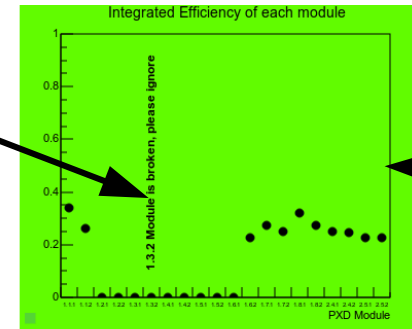
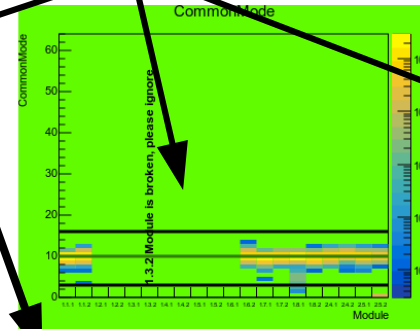
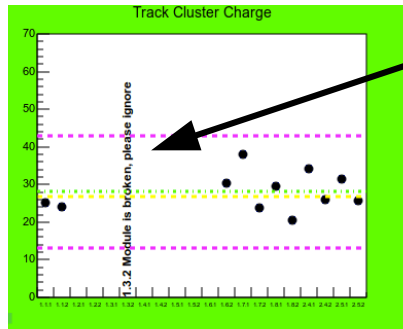
- Test on June 19th → 20th
- 1.5 hour during normal data taking, normal physics trigger
 - File is ‘beam’ on BCG request, but beside PXD and veto it is usable as physics
- Cont Injection in both rings
- Trigger Veto for HER reduced from 200 to 20 turns
- 10 settings of offset for HER scanned, each ~5 HER fills.
- > 1TB of raw data in three runs (2731, 2732, 2733)
 - Most setting changes in 2732
- 18 sensors on, only two were gated (1.03.1, 1.04.1), but all had DHP-trigger-”inhibit”
- No effect of setting sweep was seen online (rates)
 - But the rate went up a lot during GM (noise!, ped oscillations) but also because we now trigger on events near HER injection!

- No effect between different setting was seen offline
 - → offset was not set/changed in hardware!
- Effect of injected bunch clearly visible (in-sync between gates and bunch)
- HER/LER flag not set correctly (FTSW/DHC), thus we were gating with HER setting on LER, too
- Wrong unit of offset (turns instead of 127MHz ticks), but it was not set anyway
- Large noise even when doing a charge cut.
- TODO:
 - No hits expected in DHP-trigger-inhibit times → but I see (tbc)
 - Units of SC parameters compared to Trigger group (done)
 - Fix SC parameter writes to DHC hardware
 - Prepare another test → be ready if we get the opportunity

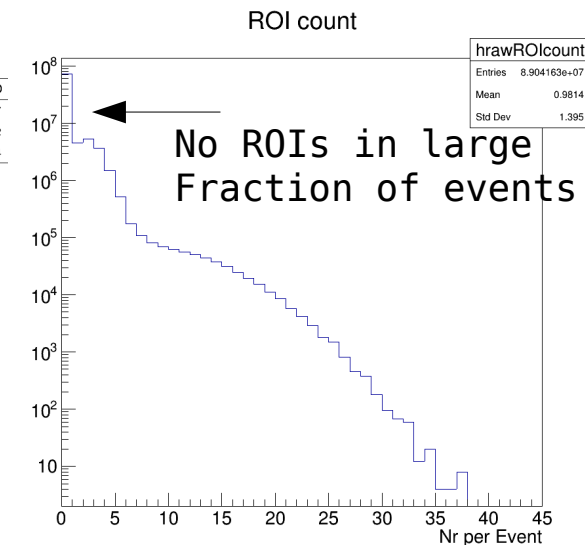
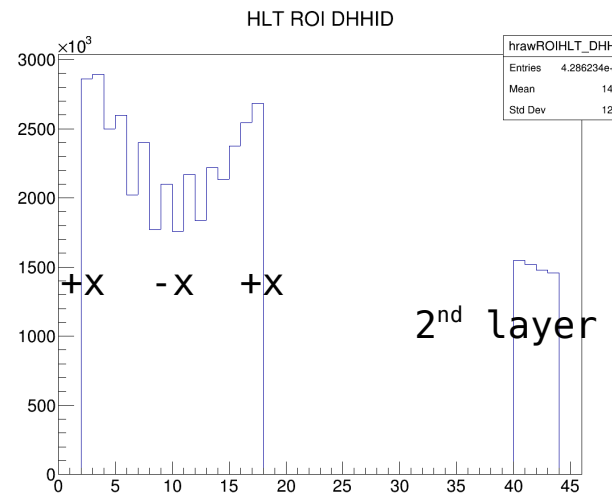
ROI Selection

- Data reduction at work!
- Effect of wrong DHE order

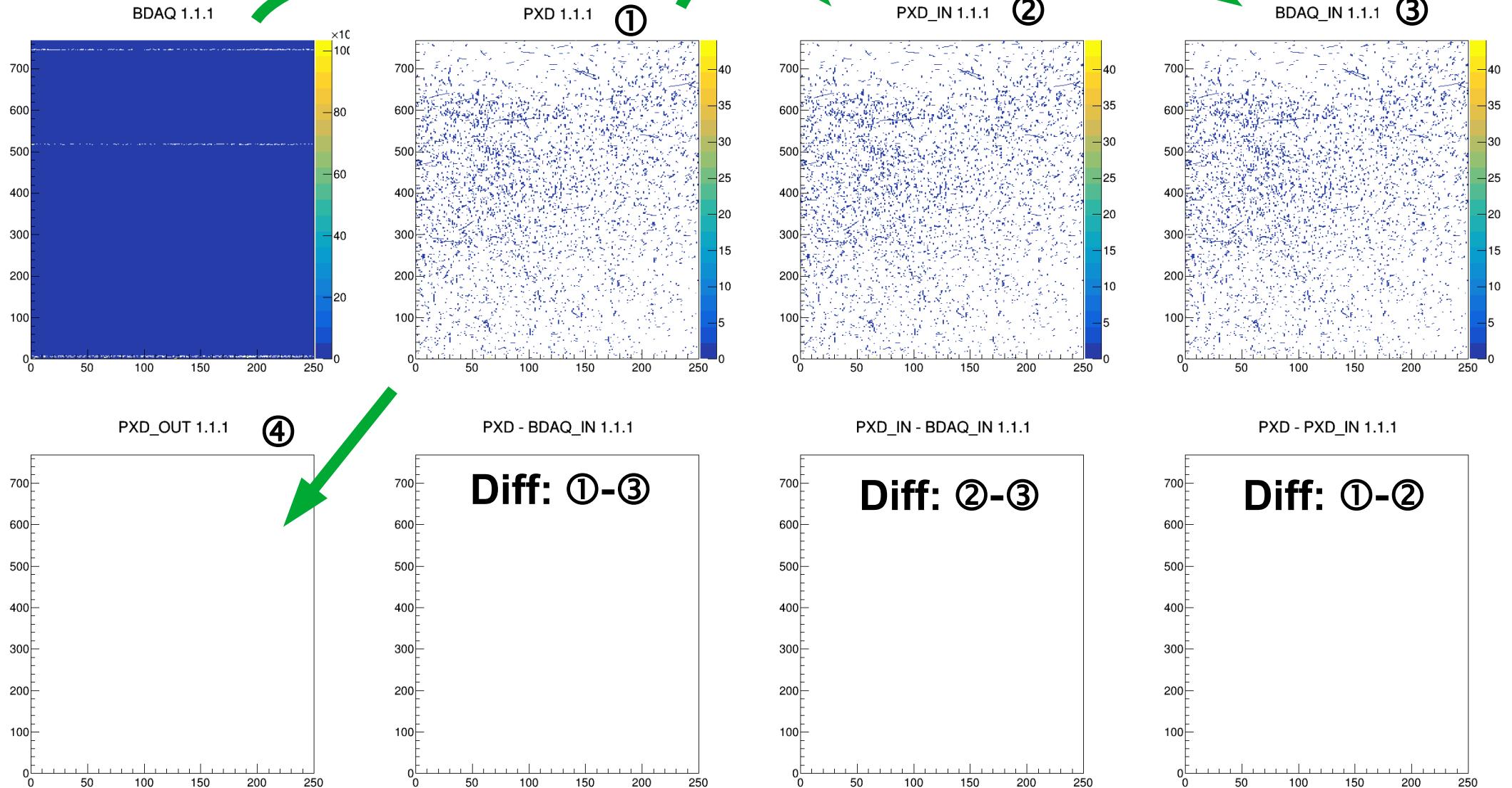
Event with errors show up as missing
Efficiency for all modules (not only
the one affected by the error



Reasonable number of ROIs and
Distribution on the modules



→ software emulation of ROI selection (on BonnDAQ and basf2 selected data)



①②③ should be identical, ④ and all the differences should be empty

- ... and so for all other modules (see backup slides)
- No hits OUTSIDE of ROIs
- No extra hits inside ROIs
- No hit is missing inside ROI
- → **everything is selected as it should**
- (but only few events 91244 within one run checked)