

TRG readout at 100 kHz

- TRG data

- GDL/GRL/CDCTRG(TSF/2D/3D/NN/ETF)/TOP
- KLMTRG might be added in LS1
- ECLTRG in ECL readout

- No zero suppression. Fixed data size.

- Only GDL data is needed for physics analysis

- Others are for DQM, calibration, development.
 - Suppressed at event level.
 - Taken per 2^n events, as much as DAQ allows. 1/256 so far.

- GDL data

- ~ 2.6 kB/ev = ~ 76 MB/sec@30kHz, >0.6 sec to be read. Cannot handle >30 kHz.
- For 100kHz, needs to increase B2L line on GDL.
 - Enough spare ports on GDL.
 - Resource on FW should be OK as GDL will move to largest UT4 (vu190).
 - No spare RJ45 port. All 4 RJ45 ports are occupied.
 - One b2tt is supposed to work for multiple B2Ls.

Hostname	TTD	DMA	DMA [kBytes]	Size [Bytes]	Rate [MB/s]	Program PCIe40
<input checked="" type="checkbox"/> rtrg1	NOTREADY	Label	#####	#####	#####	0
Belle2link-channel						
0-3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	0	0	0 2D0, 2D1, 2D2, 2D3
4-7	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	0	0	0 3D0, 3D1, 3D2, 3D3
8-11	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	0	0	0 NN0, NN1, NN2, NN3
12-15	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	0	0	0 TSF0, TSF1, TSF2, TSF3
16-19	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	0	0	0 TSF4, TSF5, TSF6, TSF8
20-23	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	0	0	0 ETF2, GDL, GRL, TOP
24-27	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	0	0	0
28-31	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	0	0	0

GDL resource on UT4 (vu160)

