Belle II Trigger/DAQ Workshop 2022

Thursday, 1 December 2022

Joint Session: Trigger and HLT (13:30 - 15:10)

   -Conveners: Taichiro Koga

time	[id] title	presenter
13:30	[45] HLT general introduction	ITOH, Ryosuke
13:50	[33] Expected L1 rate limitation and HLT processing time	TRABELSI, Karim
14:10	[34] Expected L1 rate in future	KOGA, Taichiro
14:30	[32] Level3 trigger (Fast reco)	ITOH, Ryosuke
14:50	[31] Discussion	KOGA, Taichiro

Friday, 2 December 2022

Joint Session: LS2 and Beyond 1 (09:00 - 10:30)

  -Conveners: Satoru Yamada; Taichiro Koga

time	[id] title	presenter
09:00	[114] New technologies for high-speed readout	LAI, Yun-Tsung
09:20	[107] PXD readout upgrade	KONOROV, Igor
09:40	[106] CDC FEE upgrade	TANIGUCHI, Nanae
10:00	[108] TRG upgrade	KOGA, Taichiro

Joint Session: LS2 and Beyond 2 (10:50 - 12:25)

  -Conveners: Taichiro Koga; Satoru Yamada

time	[id] title	presenter
10:50	[109] DAQ for higher L1 rate + Triggerless DAQ	YAMADA, Satoru
11:10	[119] Detector requirements for higher rate/triggerless DAQ : PXD	SPRUCK, Bjoern
11:15	[120] Detector requirements for higher rate/triggerless DAQ : SVD	NAKAMURA, Katsuro
11:20	[121] Detector requirements for higher rate/triggerless DAQ : CDC	NAKAZAWA, Yu
11:25	[122] Detector requirements for higher rate/triggerless DAQ : TOP	PURWAR, Harsh
11:30	[123] Detector requirements for higher rate/triggerless DAQ : ARICH	UNO, Kenta
11:35	[124] Detector requirements for higher rate/triggerless DAQ : ECL	REMNEV, Mikhail
11:40	[125] Detector requirements for higher rate/triggerless DAQ : KLM	BANERJEE, Swagato
11:45	[126] Detector requirements for higher rate/triggerless DAQ : TRG	NAKAZAWA, Hideyuki KOGA, Taichiro
11:50	[111] Discussion	
12:05	[112] Hardware Acceleration in HLT	ZHOU, Qidong