43rd B2GM TRG session 2022/10/5 (Wed)

Development of track finding algorithm using TFP-SVD for L1 trigger

Contents

- Motivation
- TFP-SVD and trigger algorithm
- Review of last achievement
- New study for better TRG performance

The University of Tokyo
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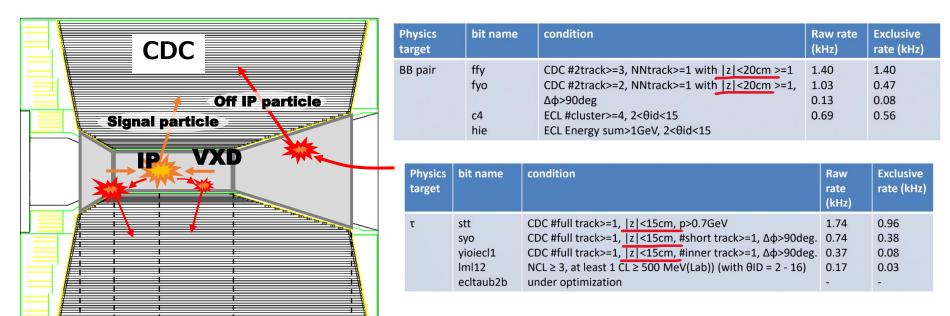
-60 -40

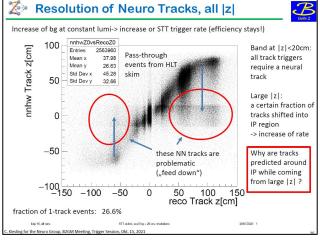
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- CDC trigger has highest trigger rate among sub-triggers of Belle II L1 trigger.
- Off IP particles are one of the sources of beam background.

150 z [cm]

CDC trigger cuts particles coming from 15 or 20cm away from IP.

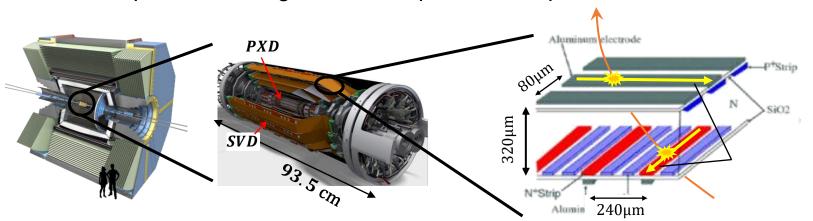


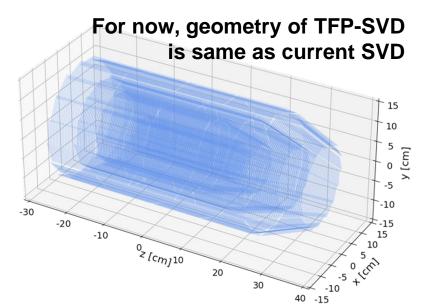


It seems that there is room to improve the current trigger

SVD

- the double-sided silicon-strip detector located in the innermost part of Belle II detectors.
- detects the position through which the particle has passed.

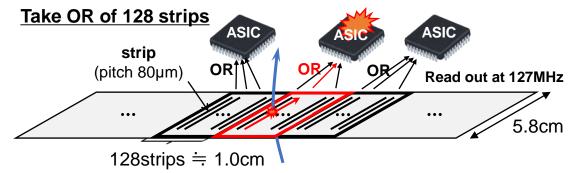




Thin-fine pitch SVD (TFP-SVD)

• We are developing new SVD to install after 2026.

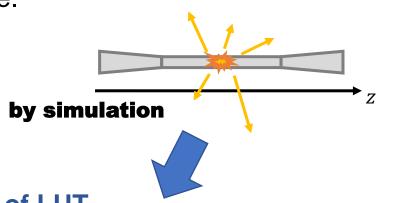
	Current SVD	TFP-SVD
pitch of P side strip	75 µm	75 µm
pitch of N side strip	160 or 240 μm	80 µm
Sampling rate	32 MHz	127 MHz
Generate TRG signal	×	0



We are considering new L1 trigger using TFP-SVD

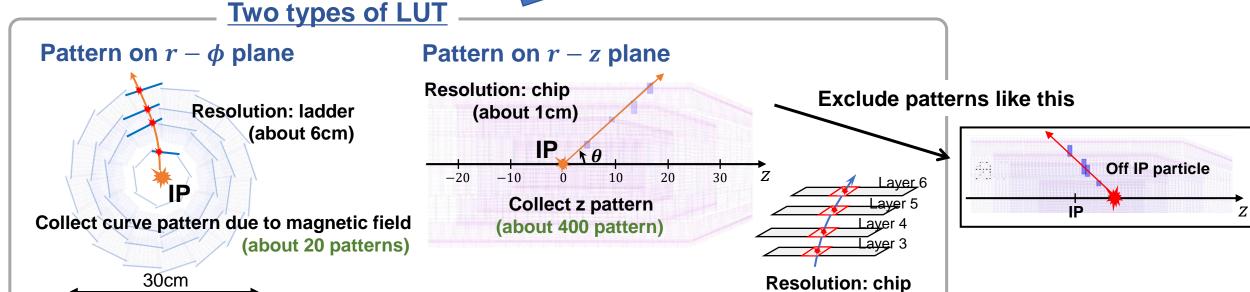
Pattern matching by LOOK UP TABLE (LUT)

- Collect a lot of track patterns of particles from IP.
- Use those as a look up table.

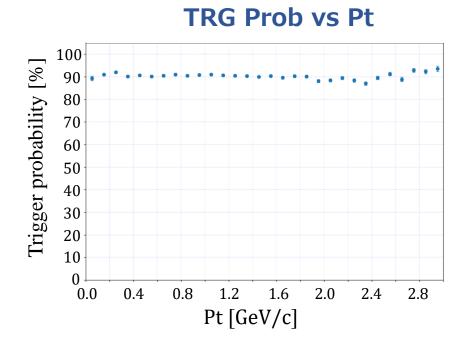


Condition of particle generation for LUT

Parameter	Condition	
Particle type	μ^\pm	
momentum p [GeV/c]	$0.2 \le p \le 3.0$	
Production point z [cm]	z = 0	

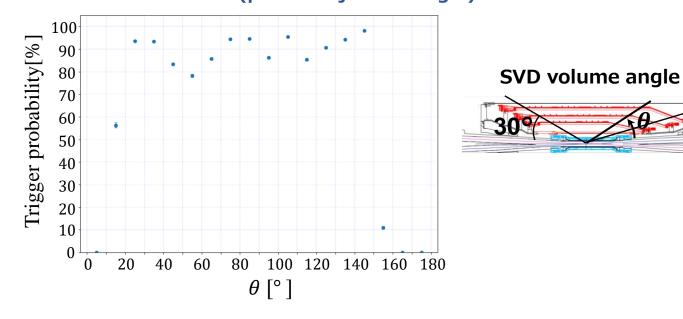


We generated only one particle at IP and investigated trigger efficiency.

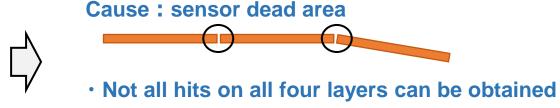


TRG efficiency is about 90%

TRG Prob vs θ (particle ejection angle)

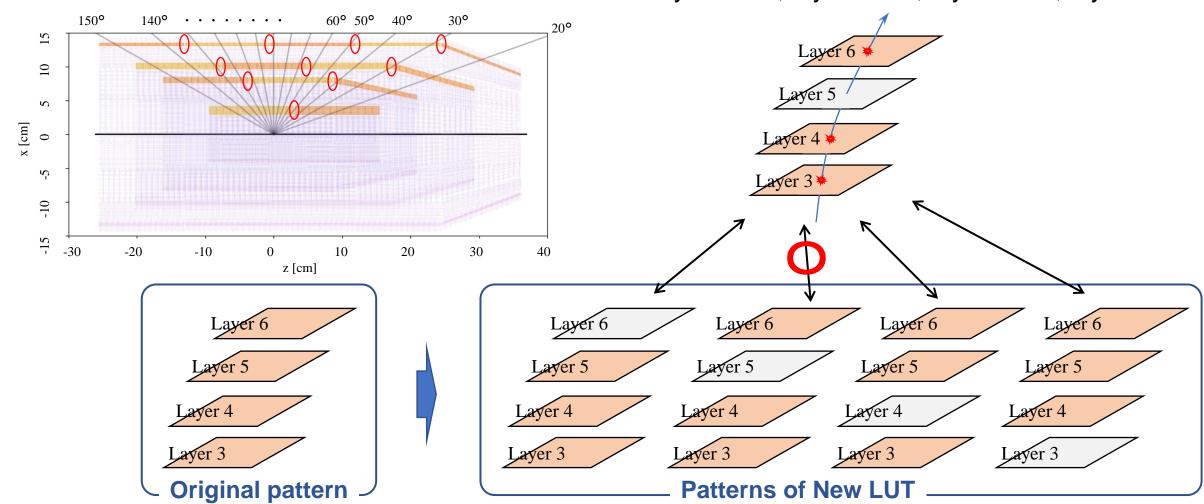


Directional efficiency



- We need to cover sensor dead area.
- Track finding algorithm using 3/4-layers: pattern matching using any three layers of four layers.

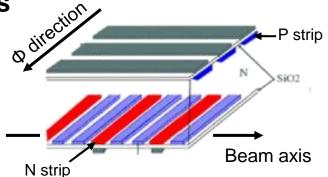
layer 3 4 5, layer 3 4 6, layer 3 5 6, layer 4 5 6



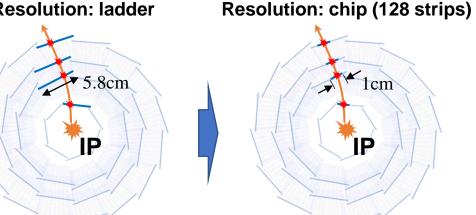
Concern

- Relaxing condition → high fake trigger under the random BG
- To enhance BG rejection power, we use more detailed φ information.

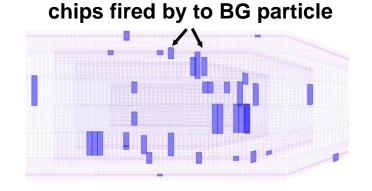
Use P strips



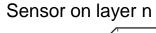
Resolution: ladder

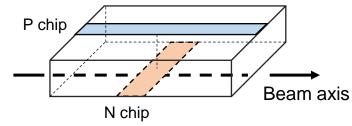


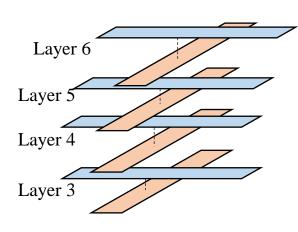
Pattern on $r - \phi$ plane



LUT using P-N strips

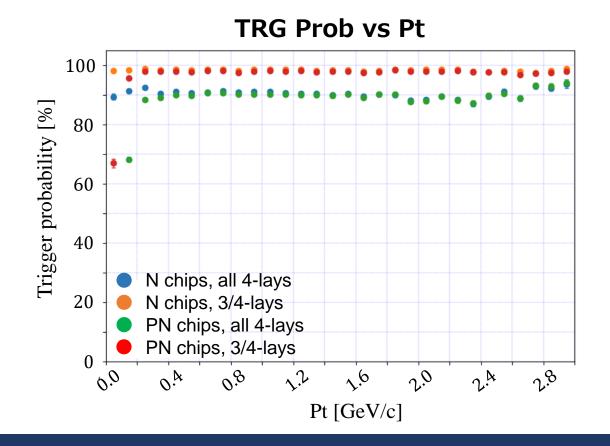


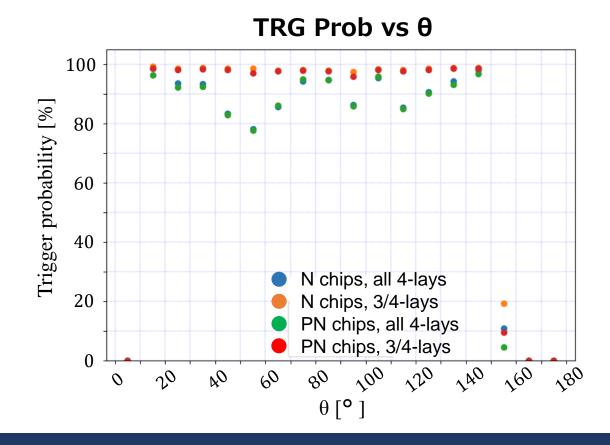




	N chips	P-N chips
4-lays	4 hits of all four layers	8 hits of all four layers
3/4-lays	3 hits of any three of the four layers	6 hits of any three of the four layers

- What I showed before
- TRG efficiency improves to nearly 100% for all θ .
- almost identical to
- almost identical to

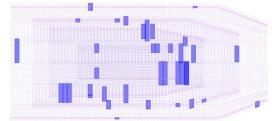


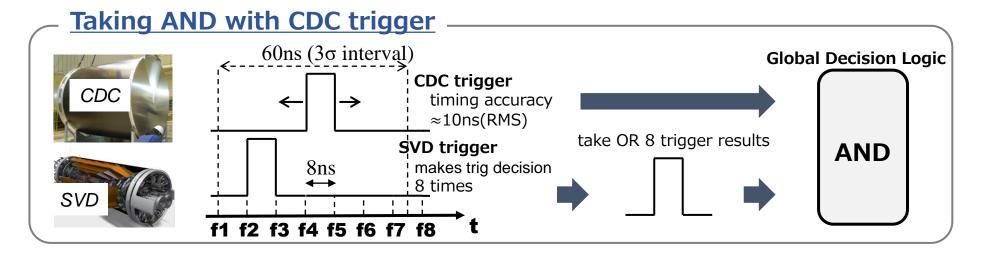


Result: Random BG study

- BG particles with low momentum can make a lot of hits → we need to consider fake trigger rate
- BG samples were prepared assuming nominal luminosity.
- We consider two types of time scale
 - Sampling rate of ASIC of TFP-SVD: 8ns
 - Timing accuracy of CDC trigger: 60ns



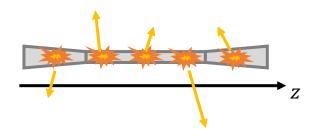




TRG Prob

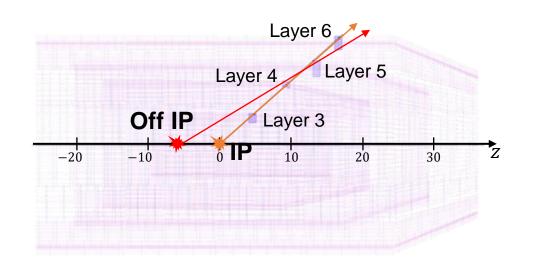
	N, 4-lays	N, 3/4-lays	P-N, 4-lays	P-N, 3/4-lays
8ns (per one frame)	5%	50%	1%	10%
60ns (taken OR of 8 frames)	22%	95%	3%	35%

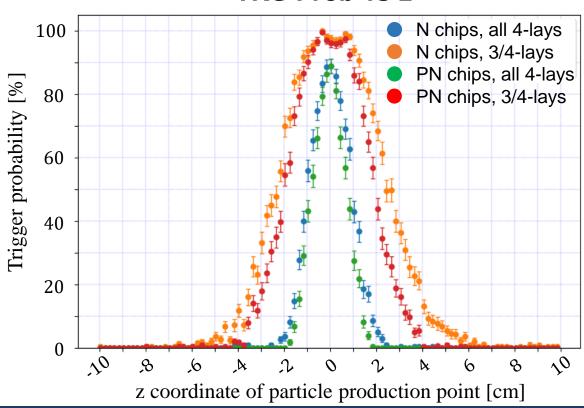
- To investigate off IP particle rejection power, we generated only one particle at various z.
- can reject particles flying from 2.5cm away from the IP
- Sharpness is somewhat lost, the boundary of the rejection region is 6cm
- slightly sharper than
 , the boundary of the rejection region is 2cm
- slightly sharper than
 , the boundary of the rejection region is 4cm



TRG Prob vs z

Sharpness is mainly supported by Layer 3





Summary

• I considered some types of algorithm to improve TRG efficiency while maintaining high BG(random BG and off IP particle) rejection power.

Trigger performance

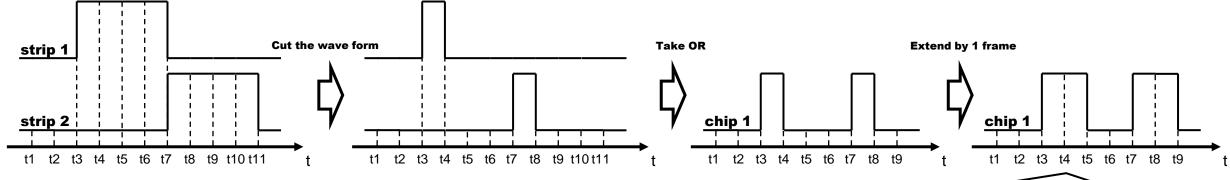
	N, all 4-lays	N, 3/4-lays	P-N, all 4-lays	P-N, 3/4-lays
Efficiency	90%	98%	90%	98%
Fake TRG prob under random BG (60ns)	22%	95%	3%	35%
Off IP rejection region	z > 2.5cm	z > 6cm	z > 2cm	z > 4cm

- I think "P-N, 3/4-lays" algorithm is best of these options.
 - sufficient efficiency
 - good off IP particle rejection power
 - prospects of stronger BG rejection power by matching with CDC

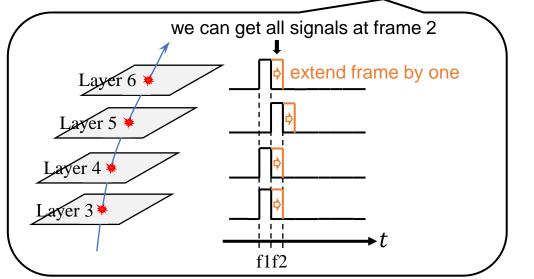
BACK UP

Read out by ASIC

- One ASIC of TFP-SVD(SNAP) will read out signals every about 8ns and will take OR.
- The timing of particle going through a sensor can be obtained from the rising edge of wave form.
- For that, before taking OR, we cut the waveform leaving rising edge.



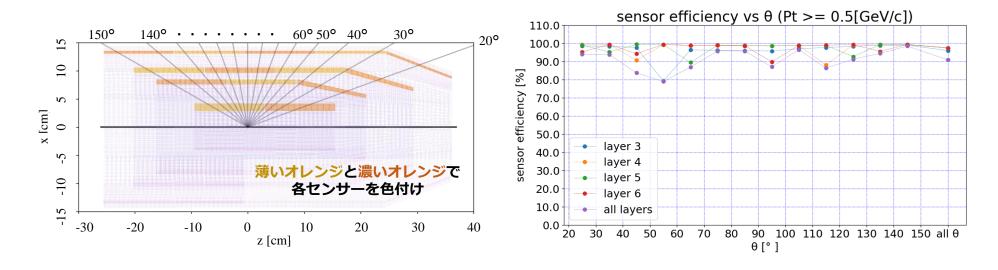
- However, the timing may be off by about one frame.
- In order not to miss a series of signals, we extended pulse rise frame by one.



• シグナルの粒子しかないのに、トリガー確率が要求値に届かなかった.

Q. センサーが反応していない??

- センサーefficicencyとして、荷電粒子が通過したときにセンサーが反応する確率 を調べた。
- センサーをまたぐ θ の範囲でセンサーefficiencyは下落し、 θ 全体では90%と、実験で得られたトリガーefficiencyの上限値と一致した。



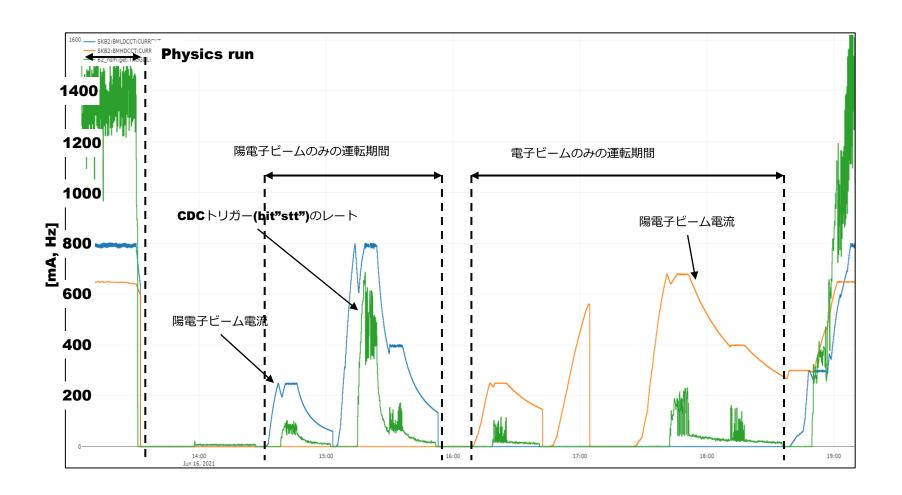
センサーefficiencyがトリガーefficiencyを決定 ⇒ヒットの欠損に対応できるアルゴリズムが必要

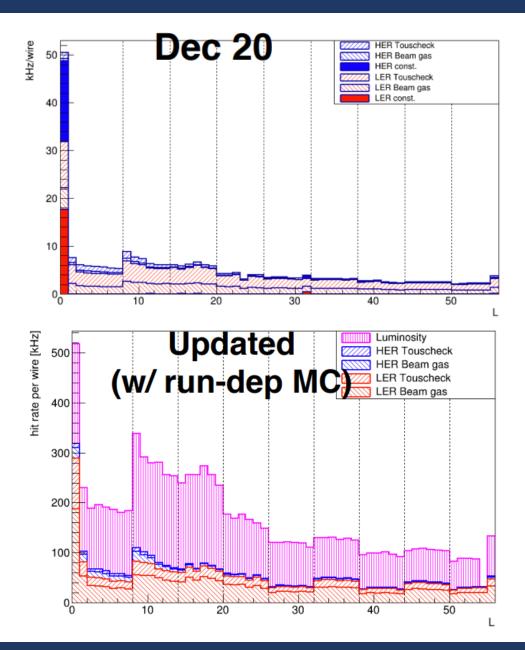
- ビームバックグラウンドは ϕ 方向ランダムにセンサーを鳴らすと考えられる.
- r φ平面で分割すると、、、

IPから飛来するシグナル粒子は同じセクションにヒット 同一セクション内の

シグナルトラック BG粒子が同じセクションにヒットを作る割合小? CDC SVDトリガー単独 では拾うが・・ 同じセクションに CDCヒットなし

CDCトリガーと ϕ マッチングを行うことで、 フェイクトリガー確率を減少させられるかも







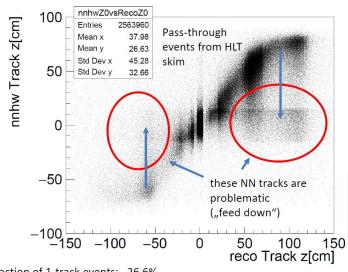
Resolution of Neuro Tracks, all |z|







Increase of bg at constant lumi-> increase or STT trigger rate (efficiency stays!)

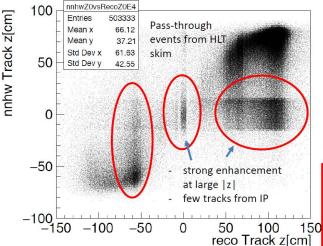


Band at |z|<20cm: all track triggers require a neural track

Large |z|: a certain fraction of tracks shifted into IP region -> increase of rate

Why are tracks predicted around IP while coming from large |z|?

Tracks from large |z| tend to miss the inner CDC layer ("SL1") -> "expert 4" network



Plot: reco tracks matched to neural tracks with missing stereo SL1

Plot: Neuro tracks selected which do not have inner stereo SL (SL1) hit (and also missed innermost axial SLO)

These tracks are dominantly coming from large |z|.

NN resolutions are about adequate

To-Do: 150 Feed-down must be reduced by improved training

fraction of 1-track events: 26.6%

STT active zcutTrig = 20 cm resolution

26/07/2021 1

C. Kiesling for the Neuro Group, B2GM Meeting, Trigger Session, Okt. 15, 2021

