



- **Enabled SPI lanes 0,1,3,4 with default pin layout following Travis' [link](#)**
 - **Enabled on boot, unable to enable otherwise**
 - **Only one chip select pin enabled**
- **Validated in two ways**
 - **Using bash script**
`raspi-gpio get`
 - **In code**
 - **Would throw error if not enabled, doesn't**

```
pi1@raspberrypi: ~  
File Edit Tabs Help  
pi1@raspberrypi:~$ raspi-gpio get  
BANK0 (GPIO 0 to 27):  
GPIO 0: level=1 fsel=1 func=OUTPUT pull=UP  
GPIO 1: level=1 fsel=7 alt=3 func=SPI3_MISO pull=UP  
GPIO 2: level=1 fsel=7 alt=3 func=SPI3_MOSI pull=UP  
GPIO 3: level=0 fsel=7 alt=3 func=SPI3_SCLK pull=UP  
GPIO 4: level=1 fsel=1 func=OUTPUT pull=UP  
GPIO 5: level=1 fsel=7 alt=3 func=SPI4_MISO pull=UP  
GPIO 6: level=1 fsel=7 alt=3 func=SPI4_MOSI pull=UP  
GPIO 7: level=0 fsel=7 alt=3 func=SPI4_SCLK pull=UP  
GPIO 8: level=1 fsel=1 func=OUTPUT pull=UP  
GPIO 9: level=0 fsel=4 alt=0 func=SPI0_MISO pull=DOWN  
GPIO 10: level=0 fsel=4 alt=0 func=SPI0_MOSI pull=DOWN  
GPIO 11: level=0 fsel=4 alt=0 func=SPI0_SCLK pull=DOWN  
GPIO 12: level=0 fsel=0 func=INPUT pull=DOWN  
GPIO 13: level=0 fsel=0 func=INPUT pull=DOWN  
GPIO 14: level=1 fsel=2 alt=5 func=TXD1 pull=NONE  
GPIO 15: level=1 fsel=2 alt=5 func=RXD1 pull=UP  
GPIO 16: level=0 fsel=0 func=INPUT pull=DOWN  
GPIO 17: level=0 fsel=0 func=INPUT pull=DOWN  
GPIO 18: level=1 fsel=1 func=OUTPUT pull=DOWN  
GPIO 19: level=0 fsel=3 alt=4 func=SPI1_MISO pull=DOWN  
GPIO 20: level=0 fsel=3 alt=4 func=SPI1_MOSI pull=DOWN  
GPIO 21: level=0 fsel=3 alt=4 func=SPI1_SCLK pull=DOWN  
GPIO 22: level=0 fsel=0 func=INPUT pull=DOWN  
GPIO 23: level=0 fsel=0 func=INPUT pull=DOWN  
GPIO 24: level=0 fsel=0 func=INPUT pull=DOWN
```